



Customer Information Notification

201405009I

Issue Date: 16-May-2014

Effective Date: 16-Jun-2014

Here's your personalized quality information concerning products Digi-Key purchased from NXP. For detailed information we invite you to [view this notification online](#)



QUALITY

Management Summary

The change defines the I2C-bus address by metal mask to fixed logic levels. Before one bit in the OTP memory selected upon power-on.

Change Category

<input type="checkbox"/> Wafer Fab process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input checked="" type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Electrical spec./Test coverage	<input type="checkbox"/> Mechanical Specification
<input type="checkbox"/> Wafer Fab location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Test Location	<input type="checkbox"/> Packing/Shipping/Labeling

PCF8523T fix in I2C address definition

Information Notification

- When addressing the circuit before the internal power-on-reset was completed it was possible that the RTC was not reacting correctly.
- In the existing version the I2C-bus address is fetched at power up from the OTP memory. This change fixes now the I2C-bus address by metal mask to static logic levels.
- Defining the address to a fix value: A2, guarantees that the I2C-bus address A2 is recognized as soon as internal supply voltage reaches VDDmin.

Why do we issue this Information Notification

To ensure correct I2C address in all cases.

Identification of Affected Products

Product identification does not change

Impact

It is a correction of a possible error in power-up. Correct functionality is better ensured.

Data Sheet Revision

No impact to existing datasheet

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Cengiz Oezcan
Position Quality Manager PL CWG
e-mail address cengiz.oezcan@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

A global semiconductor company with operations in more than 25 countries, NXP posted unaudited revenue of \$4.36 billion in 2012.

You have received this email because you are a designated contact or subscribed to NXP's Quality Notifications. NXP shall not be held liable if this Notification is not correctly distributed within your organization.

This message has been automatically distributed. Please do not reply.

[View Notification](#)

[Subscription](#)

[Support](#)

[NXP](#) | [Privacy Policy](#) | [Terms of Use](#)

NXP Semiconductors
High Tech Campus, 5656 AG Eindhoven, The Netherlands

© 2006-2010 NXP Semiconductors. All rights reserved.