## Features

- Frequency:
- Output Type:
- Frequency Stability:
- Supply Voltage:
- Standard Packages:
- RM S phase jitter:
- Operating Temperature:
161.133 M Hz


## LVDS

$\pm 50 \mathrm{ppm}$
$2.5 \mathrm{~V} \& 3.3 \mathrm{~V}$
$5.0 \times 3.2 \mathrm{~mm} ; 7.0 \times 5.0 \mathrm{~mm}$
0.7 ps typical ( 12 k to 20 MHz )
-40 to $85^{\circ} \mathrm{C}$


This product is rated "Green", please contact IDT for environmental compliancy information

## Specification

| Parameter | 2.5 V Specifications |  |  | Specifications |  |  | Units | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ | Max | Min | Typ | Max |  |  |
| Supply Voltage ( $\mathrm{V}_{\mathrm{DD}}$ ) |  | 2.50 |  |  | 3.30 |  | V |  |
| Output Frequency |  | 161.133 |  |  | 161.133 |  | MHz |  |
| Frequency Stability |  |  | $\pm 50$ |  |  | $\pm 50$ | ppm | -40 to $85^{\circ} \mathrm{C}$ |
| Supply Current |  | 95 |  |  | 95 |  | mA | No load |
| Input LOW level |  |  | $0.3 \mathrm{~V}_{\text {DD }}$ |  |  | $0.3 \mathrm{~V}_{\mathrm{DD}}$ | V | At OE pin |
| Input HIGH level | 0.7V $\mathrm{V}_{\mathrm{DD}}$ |  |  | 0. $7 \mathrm{~V}_{\mathrm{DD}}$ |  |  | V | At OE pin |
| Output LOW level |  | 1.075 |  |  | 1.075 |  | V |  |
| Output HIGH level |  | 1.425 |  |  | 1.425 |  | V |  |
| Amplitude ( $\mathrm{V}_{\mathrm{A}}$ ) |  | 0.35 |  |  | 0.35 |  | V | Single Ended output swing (Pk-Pk) |
| M id Level ( $\mathrm{V}_{\mathrm{M}}$ ) |  | 1.25 |  |  | 1.25 |  | V |  |
| Rise Time ( $T_{R}$ ) |  | 400 |  |  | 400 |  | ps | M aximum; 20/80\% of $\mathrm{V}_{\mathrm{A}}$; Output load (CL) $=2 \mathrm{pF}$ |
| Fall Time ( $\mathrm{T}_{\mathrm{F}}$ ) |  | 400 |  |  | 400 |  | ps | M aximum; 20/80\% of $\mathrm{V}_{\mathrm{A}}$; Output load (CL) $=2 \mathrm{pF}$ |
| Symmetry (SYM) | 45 |  | 55 | 45 |  | 55 | \% | Worst case; measured at 50\% of waveform |
| Period Jitter |  | 5 |  |  | 5 |  | ps | M easured over 10k cycles, rms |
| Phase Jitter |  |  | 1 |  |  | 1 | ps | 12k to 20 M Hz , rms |
| Aging |  |  | $\pm 5$ |  |  | $\pm 5$ | ppm | $25^{\circ} \mathrm{C}, 10$ years |

Note: Above specifications are typical at room temperature $\left(25^{\circ} \mathrm{C}\right)$ unless otherwise specified. Frequency stability includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, and aging ( $+25^{\circ} \mathrm{C}, 10$ years).

Block Diagram


## Part Ordering Information

| Package Size | Voltage | Ordering Code |
| :---: | :---: | :---: |
| $5.0 \times 3.2 \mathrm{~mm}$ | 3.3 V | 4 M A161133Z4AACTGI |
|  | 2.5 V | 4 M A161133Z4BACTGI |
| $7.0 \times 5.0 \mathrm{~mm}$ | 3.3 V | 4 M A161133Z4AACUGI |
|  | 2.5 V | 4 M A161133Z4BACUGI |



Package Outline and Dimensions




 third parties.
 a manner does so at their own risk, absent an express, written agreement by IDT
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