

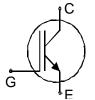
IGBT Chip in NPT-technology

FEATURES:

- 600V NPT technology
- 100µm chip
- short circuit prove
- positive temperature coefficient
- easy paralleling

This chip is used for:

• SGP20N60



Applications:

• drives

Chip Type	V _{CE}	I _{Cn}	Die Size	Package	Ordering Code
SIGC18T60SNC	600V	20A	4.3 x 4.3 mm ²	sawn on foil	Q67041-S2856- A001
SIGC18T60SNC	600V	20A	4.3 x 4.3 mm ²	unsawn	Q67041-S2856- A002

MECHANICAL PARAMETER:

Raster size	4.3 x 4.3	mm ²			
Area total / active	18.49 / 14.3	1			
Emitter pad size	2.48 x 2.98	1			
Gate pad size	0.7 x 1.08]			
Thickness	100	μm			
Wafer size	150	mm			
Flat position	270	deg			
Max.possible chips per wafer	796				
Passivation frontside	Photoimide				
Emitter metallization	3200 nm Al Si 1%				
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	Al, ≤500μm				
Reject Ink Dot Size	Ø 0.65mm; max 1.2mm				
Recommended Storage Environment	nt store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T _j =25 °C	V _{CE}	600	V
DC collector current, limited by T _{jmax}	I _C	1)	А
Pulsed collector current, t _p limited by T _{jmax}	I _{cpuls}	60	А
Gate emitter voltage	V _{GE}	±20	V
Operating junction and storage temperature	T_j , T_{stg}	-55 + 150	°C

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_i =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
Tarameter	Cymbol		min.	typ.	max.	01
Collector-emitter breakdown voltage	V _{(BR)CES}	V _{GE} =0V, I _C =500μA	600			
Collector-emitter saturation voltage	V _{CE(sat)}	V _{GE} =15V, I _C =20A	1.6	1.9	2.5	V
Gate-emitter threshold voltage	V _{GE(th)}	$I_C=500\mu A,\ V_{GE}=V_{CE}$	3	4	5	
Zero gate voltage collector current	I _{CES}	V _{CE} =600V, V _{GE} =0V			1.5	μA
Gate-emitter leakage current	I _{GES}	V _{CE} =0V, V _{GE} =20V			120	nA

DYNAMIC CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
raiailletei	Symbol	Conditions	min.	typ.	max.	Oilit
Input capacitance	Ciss	V _{CE} =25V	-	1100	1320	pF
Output capacitance	Coss	V _{GE} =0V	-	107	128	
Reverse transfer capacitance	Crss	f=1MHz	-	63	75	

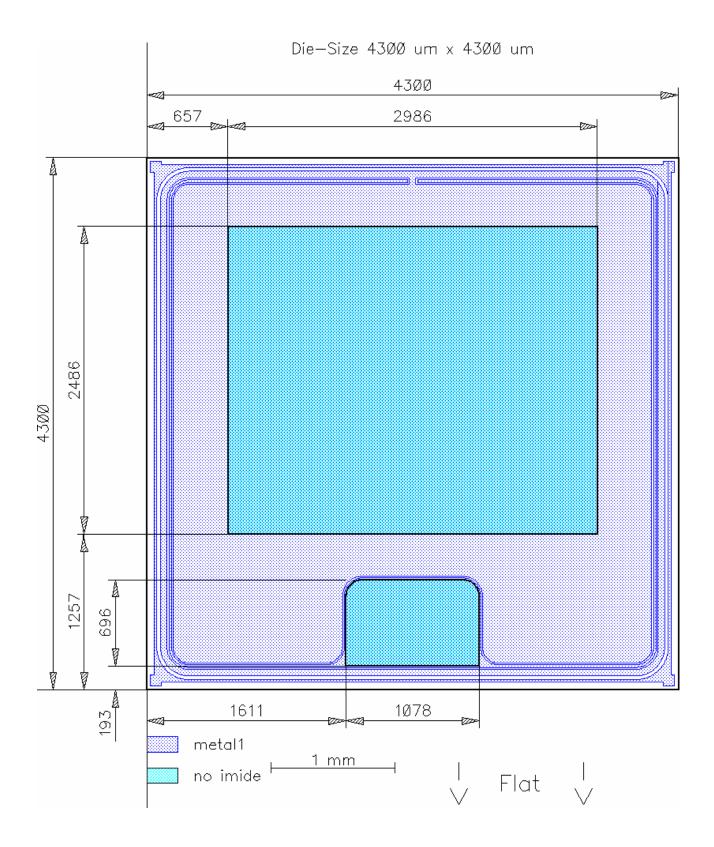
SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

Parameter	Symbol	Conditions ²⁾	Value			Unit
1 arameter			min.	typ.	max.	
Turn-on delay time	$t_{d(on)}$	$T_{\rm j}$ =150°C $V_{\rm CC}$ =400V	-	36	46	ns
Rise time	t_{r}	I _C =20A	-	30	36	
Turn-off delay time	$t_{d(off)}$	$V_{\rm GE}$ =+15/0V $R_{\rm G}$ =16 Ω	-	250	300	
Fall time	t_{f}	7.6- 1.011	-	63	76	

²⁾ switching conditions different to 600V Standard IGBT 2, under comparable switching conditions 40% faster turnoff than Standard IGBT 2. Values also influenced by parasitic L- and C- in measurement and package.



CHIP DRAWING:





FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the	SGP20N60	Packago :TO220
device data sheet	SGFZUNOU	Package :TO220

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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