

Features

- Advanced Trench Cell Design
- Low Thermal Resistance
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

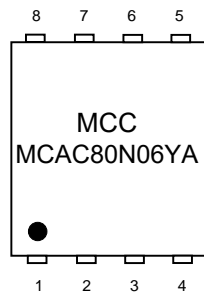
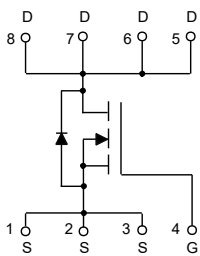
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 1.47°C/W Junction to Case (Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	80	A
T _C =25°C			
Pulsed Drain Current (Note 3)	I_{DM}	320	A
Total Power Dissipation	P_D	85	W
T _C =25°C			

Note:

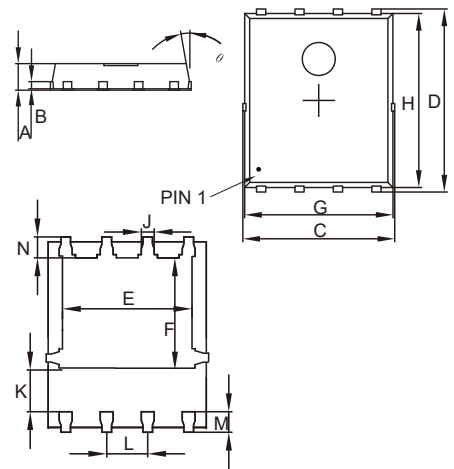
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. The Maximum Current Rating is Package Limited.

Internal Structure and Marking Code



**N-CHANNEL
MOSFET**

DFN5060



DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	0.031	0.047	0.80	1.20	
B	0.010		0.254		TYP.
C	0.193	0.222	4.90	5.64	
D	0.232	0.250	5.90	6.35	
E	0.148	0.167	3.75	4.25	
F	0.126	0.154	3.20	3.92	
G	0.189	0.213	4.80	5.40	
H	0.222	0.239	5.65	6.06	
K	0.045	0.059	1.15	1.50	
J	0.012	0.020	0.30	0.50	
L	0.046	0.054	1.17	1.37	
M	0.012	0.028	0.30	0.71	
N	0.016	0.028	0.40	0.71	

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	60			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=48V, V_{GS}=0V, T_J=25^\circ C$			1	μA
		$V_{DS}=48V, V_{GS}=0V, T_J=85^\circ C$			30	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.5		2.5	V
Drain-Source On-Resistance ^(Note 4)	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		2.2	2.5	m Ω
		$V_{GS}=4.5V, I_D=10A$		3.3	4.5	
Diode Characteristics						
Diode Forward Voltage ^(Note 4)	V_{SD}	$V_{GS}=0V, I_S=20A$			1.3	V
Continuous Body Diode Current	I_S				80	A
Reverse Recovery Time	t_{rr}	$I_{SD}=20A, di_{SD}/dt=100A/\mu s$		67		ns
Reverse Recovery Charge	Q_{rr}				90	
Dynamic Characteristics^(Note 5)						
Input Capacitance	C_{iss}	$V_{DS}=30V, V_{GS}=0V, f=1MHz$		4894		pF
Output Capacitance	C_{oss}			2208		
Reverse Transfer Capacitance	C_{rss}			171		
Total Gate Charge	Q_g	$V_{DS}=30V, V_{GS}=10V, I_D=20A$		99		nC
Gate-Source Charge	Q_{gs}			16		
Gate-Drain Charge	Q_{gd}			27		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=30V, V_{GEN}=10V, R_G=4.5\Omega, R_L=1.5\Omega, I_{DS}=20A$		14		ns
Turn-On Rise Time	t_r			36		
Turn-Off Delay Time	$t_{d(off)}$			75		
Turn-Off Fall Time	t_f			50		

Note:

- Pulse Test; Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
- Guaranteed by Design, Not Subject to Production Testing.

Curve Characteristics

Fig. 1 - Typical Output Characteristics

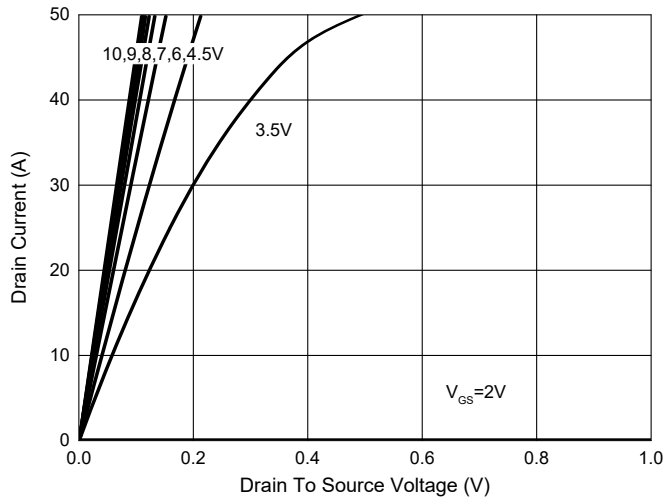


Fig. 2 - $I_S - V_{SD}$

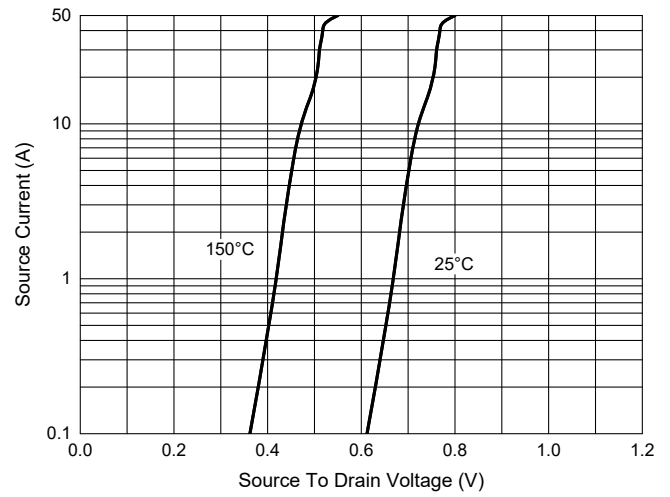


Fig. 3 - $R_{DS(ON)} - I_D$

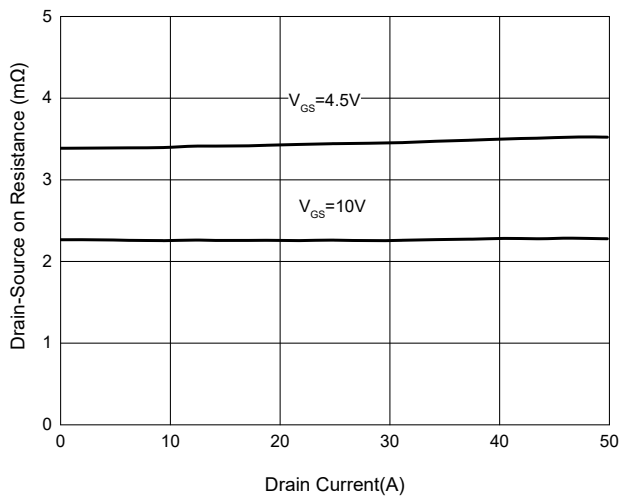


Fig. 4 - Normalized On Resistance Characteristics

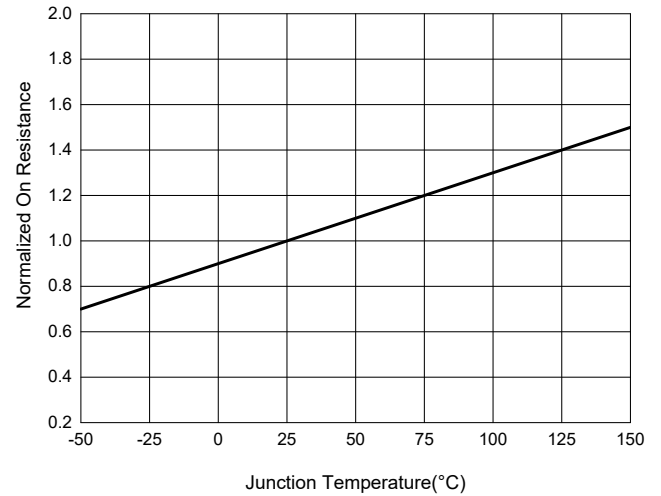


Fig. 5 - Capacitance Characteristics

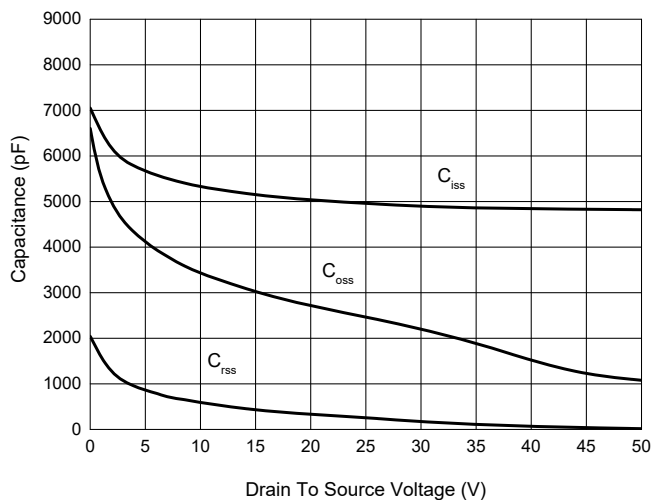
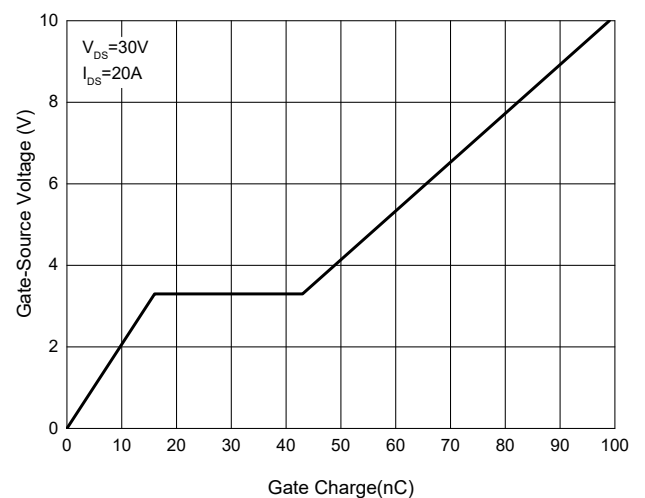
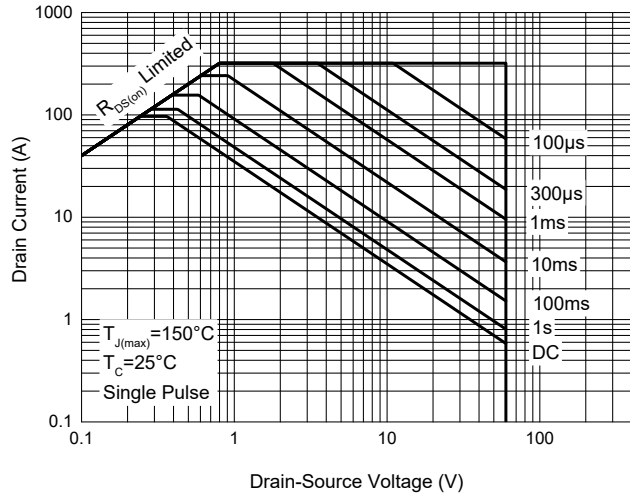


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Safe Operation Area



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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