## 3-INPUT VIDEO SUPER IMPOSER

## ■ GENERAL DESCRIPTION

The NJM2248 is 3-input video switch for video and audio signal. Two input terminals have sink-chip clamp function and so it is applied to fixed DC level of video signal. The other input terminal is transistor base input for luminant signal and so luminant level may be easily fixed by outer circuit. Its operating supply voltage range is 4.75 to 13 V and bandwidth is 10 MHz . Cross-talk is 70 dB (at 4.43 MHz ).

## - FEATURES

- Operating Voltage
$(+4.75 \mathrm{~V}$ to $+13 \mathrm{~V})$
- 3 Input-1 Output
- Internal Clamp Function $\left(\mathrm{V}_{\mathbb{N}} 1, \mathrm{~V}_{\mathbb{N}} 2\right)$
- Internal Luminance Signal Control Function $\left(\mathrm{V}_{\mathbb{N}} 3\right)$
- Cross - talk 70 dB (at 4.43 MHz )
- Wide Frequency Range
- Package Outline DIP8, DMP8 SIP8, (SSOP8)
- Bipolar Technology


## - APPLICATION

- VCR, Video Camera, AV-TV, Video Disc Player


## - PACKAGE OUTLINE



NJM2248D


NJM2248V


NJM2248M


NJM2248L

PIN CONFIGURATION


BLOCK DIAGRAM


■ INPUT CONTROL SIGNAL-OUTPUT SIGNAL

| SW1 | SW2 | OUTPUT SIGNAL |
| :---: | :---: | :---: |
| $L$ | $L$ | $V_{\mathbb{N} 1}$ |
| $H$ | $L$ | $V_{\mathbb{N} 2}$ |
| $L / H$ | $H$ | $V_{\mathbb{N}} 3$ |

■ ABSOLUTE MAXIMUM RATINGS
$\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$

| PARAMETER | SYMBOL | RATINGS | UNIT |
| :--- | :---: | :---: | :---: |
| Supply Voltage | $\mathrm{V}^{+}$ | 15 | V |
| Power Dissipation | $\mathrm{PD}_{\mathrm{D}}$ | (DIP8) 500 | mW |
|  |  | (DMP8) 300 | mW |
|  |  | (SSOP8) 250 | mW |
|  |  | (SIP8) 800 | mW |
| Operating Temperature Range | $\mathrm{T}_{\text {opr }}$ | -20 to +75 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

- ELECTRICAL CHARACTERISTICS
$\left(\mathrm{V}^{+}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$

| PARAMETERS | SYMBOLS | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Recommended Supply Voltage | $\mathrm{V}^{+}$ |  | 4.75 | - | 13.0 | V |
| Operating Current | Icc | $\mathrm{S} 1=\mathrm{S} 2=\mathrm{S} 3=\mathrm{S} 4=\mathrm{S} 5=1$ | - | 10.5 | 14.0 | mA |
| Voltage Gain | Gv | $\mathrm{V}_{1}=2.5 \mathrm{~V}_{\text {PP, }} 100 \mathrm{kHz}, \mathrm{V}_{\mathrm{o}} / \mathrm{V}_{1}$ | -0.5 | - | +0.5 | dB |
| Frequency Characteristics | $\mathrm{G}_{\mathrm{f}}$ | $\mathrm{V}_{1}=2.0 \mathrm{~V}_{\text {PP, }}, \mathrm{V}_{\mathrm{O}}(10 \mathrm{MHz}) / \mathrm{V}_{\mathrm{O}}(100 \mathrm{MHz})$ | -1.0 | 0 | +1.0 | dB |
| Differential Gain | DG | $\mathrm{V}_{1}=2 \mathrm{~V}_{\text {P. }}$, Staircase signal | - | 0 | - | \% |
| Differential Phase | DP | $\mathrm{V}_{1}=2 \mathrm{~V}_{\text {PPP, }}$, Staircase signal | - | 0 | - | deg |
| Cross-talk | CT | $\mathrm{V}_{1}=2.0 \mathrm{~V}_{\text {P-P, }} 4.43 \mathrm{MHz}, \mathrm{V}_{\mathrm{O}} / \mathrm{V}_{1}$ (Note 1) | - | -70 | - | dB |
| Switch Change Voltage | $\mathrm{V}_{\mathrm{CH}}$ | All inside SW: ON | 2.4 | - | - | V |
|  | $V_{\text {CL }}$ | All inside SW : OFF | - | - | 0.8 | V |
| Output Impedance | Ro |  | - | 10 | - | $\Omega$ |

(Note 1) : Tested on all combination except three below.
a) $S 1=2, S 4=S 5=1$ b) $S 2=2, S 4=2, S 5=1$
c) $S 3=2, S 5=2$
(Note 2) : Unless specified, tested with $\mathrm{V}_{\text {BIAS }}=3 \mathrm{~V}$.
(Note 3) : If it is not shown about switch condition, it is tested on three condition below.

$$
\text { a) } \mathrm{S} 1=2, \mathrm{~S} 2=\mathrm{S} 3=\mathrm{S} 4=\mathrm{S} 5=1 \text { b) } \mathrm{S} 1=1, \mathrm{~S} 2=2, \mathrm{~S} 3=1, \mathrm{~S} 4=2, \mathrm{~S} 5=1 \quad \text { c) } \mathrm{S} 1=\mathrm{S} 2=1, \mathrm{~S} 3=2, \mathrm{~S} 4=1 \text { or } 2, \mathrm{~S} 5=2
$$

(Note 4) : Clamp voltage of Vin1 and Vin2 is about $2 / 5$ of supply voltage (about 2.0 V if $\mathrm{V}^{+}=5 \mathrm{~V}$ ).

## - SPECIAL CARES TO BE TAKEN WHEN APPLICATION

Oscillation Prevention on light loading conditions
Recommended under circuit.


## - TEST CIRCUIT



## - TERMINAL FUNCTION

PINNO. PIN SYMBOL

## ■ APPLICATION

This IC requires $1 \mathrm{M} \Omega$ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.


This IC requires 0.1 uF capacitor between INPUT and GND, $1 \mathrm{M} \Omega$ resistance between INPUT and GND for clamp type input at mute mode.

[CAUTION]
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