



BLDC-GEVK: BLDC Motor Driver Shield Evaluation Kit

The BLDC-GEVK evaluates the LV8907, a high performance, sensor-less three-phase BLDC motor controller with integrated gate drivers for driving external N-MOSFETs. An on-chip two-stage charge pump provides required gate current for a wide range of ultra low RDS(ON) type external N-MOSFETs. The device offers a rich set of system protection and diagnostic functions such as over-current, over-voltage, short-circuit, under-voltage, over-temperature and many more. It supports open-loop as well as closed-loop speed control with user configurable startup, speed setting and proportional/integral (PI) control coefficients, making it suitable for a wide range of motor and load combinations. With an in-built linear regulator for powering an external circuit, a watchdog timer and a Local Interconnect Network (LIN) transceiver, the LV8907 offers the smallest system solution footprint. An SPI interface is provided for parameter setting and monitoring the system health and utilized through the PCA9655E's 16 bits of General Purpose parallel Input and Output (GPIO) expansion.



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