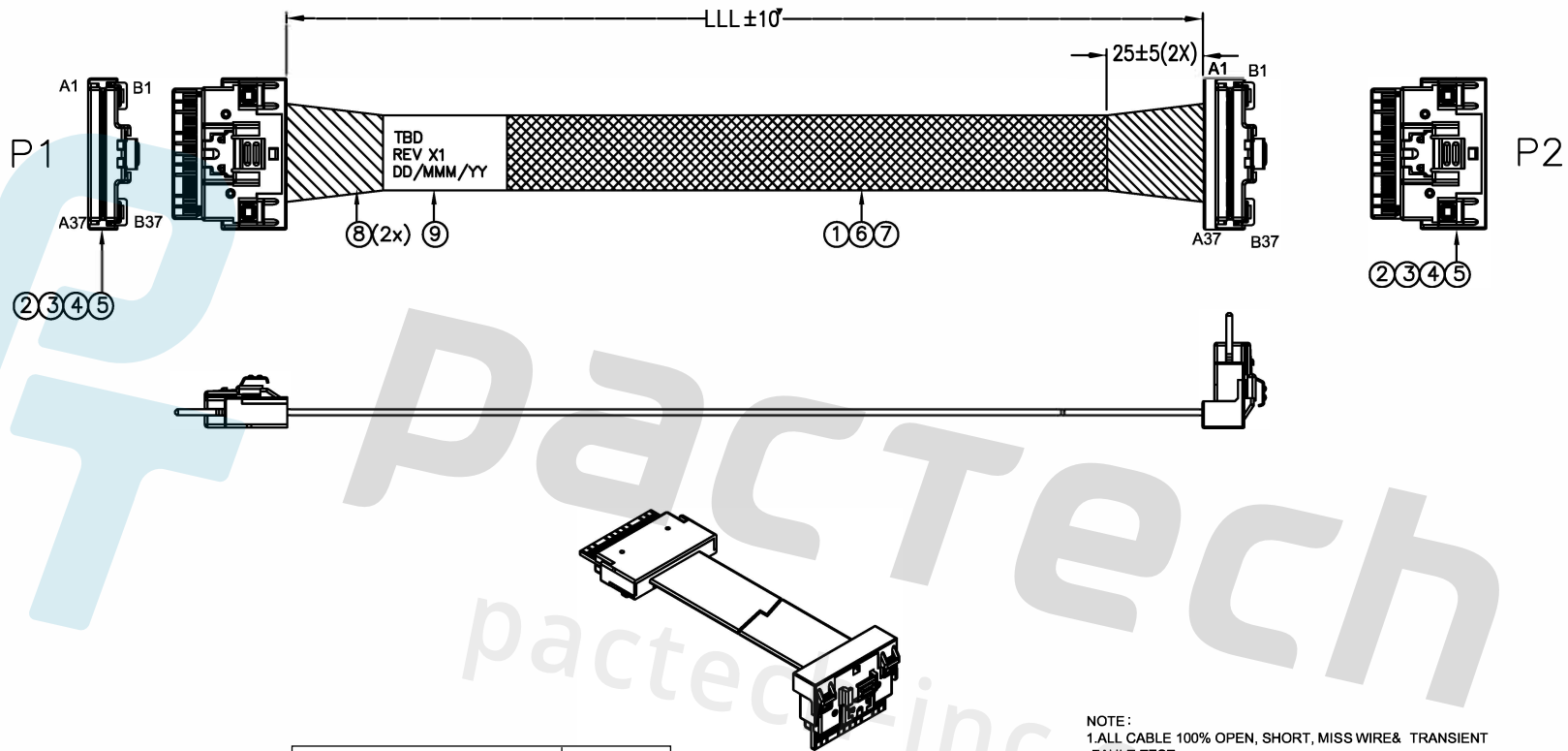


ALL PARTS MUST MEET TO RWS REQUIREMENTS

FOR CUSTOM MADE ITEMS ORDER:

- \* MINIMUM \$200 VALUE
- \* NCNR (NON-CANCELABLE & NON-RETURNABLE)
- \* ONE TIME TOOLING CHARGE, IF ANY, MUST BE APPROVED WITH AUTHORIZED SIGNATURE & DATE
- \* TURN AROUND TIME COUNTED ARO (AFTER RECEIVED FA OR DRAWING APPROVAL)

REVISION			
REV	DESCRIPTION	DATE	APPROVED
X1	Internal Release	9/1/2021	Damon



NOTE:  
 1. ALL CABLE 100% OPEN, SHORT, MISS WIRE & TRANSIENT FAULT TEST;  
 2. THE DIMENSIONS MARKED ▼ SHALL BE CHECKED ;  
 3. CONDUCTOR RESISTANCE :2.0 ohm Max.;  
 4. INSULATION RESISTANCE :10 Mohm Min/300VDC@10ms;  
 5. HIGH FREQUENCY CHARACTERISTIC PLEASE FOLLOW PCIe Gen 4 INDUSTRIAL STANDARD.  
 Connector Impedance: 85±15 ohm . Cable Impedance: 85±5 ohm

APPROVED BY \_\_\_\_\_  
 COMPANY: \_\_\_\_\_  
 PURCHASING/ENGINEER: \_\_\_\_\_  
 DATE: \_\_\_\_\_

ITEM NO.	LENGTH (LLL)
PT-SAS-SL8-RA-250	250mm ± 15
PT-SAS-SL8-RA-500	500mm ± 15
PT-SAS-SL8-RA-750	750mm ± 15
PT-SAS-SL8-RA-1000	1000mm ± 15

NO	NAMF	MATFRIAL	DFSCRIPTION	O'TY	UNIT
⑩	FLAT CABLE	FLAT CABLE,32AWGx2C,VW-1,PH=0.6mm,R2		AR	M
⑨	LABEL	LxW=70x14mm,PP,WHITE,WITH BLACK TEXT,R2		1	PCS
⑧	AC TAPE	BLACK AC TAPE T=0.12mm,R2		AR	M
⑦	PET	OD=9.5mm,PET,BLACK,VW-1,R2		AR	M
⑥	FLAT CABLE	FLAT CABLE,32AWGx5C,BLACK,VW-1,PH=0.6mm,R2		AR	M
⑤	PA	PA6+20%GF,BLACK,UL94V-0,R2		AR	KG
④	PE	PE-LOW DENSITY,NATURAL,R2		AR	KG
③	LATCH	LATCH,SLIMLINE MINI SAS LATCH SUS301 3/4H, T=0.25mm,R2		2	PCS
②	PCB	Slimline SAS 74PIN STR PCB,85ohm,TU862,Au30u'',Ni 100U*Min,R2		2	PCS
①	SAS CABLE	SAS3.0 CABLE,UL20744,(30AWGx2C+2EAM)X1P,BLUE,85U,VW-1,12G,R2		AR	M

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DESCRIPTION		SlimSAS 8i STR to SlimSAS 8i RA CABLE ASSEMBLY	
CUSTOMER PART NUMBER			
DRAWN BY Man Hin Wong	CHECKED BY D.Choi	SIZE A	PART NUMBER PT-SAS-SL8-RA-LLL
APPROVED BY A. Chui	UNIT Millimeter	SCALE	REV X1
SHEET			Page 1/2

FOR CUSTOM MADE ITEMS ORDER:

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REVISION			
REV	DESCRIPTION	DATE	APPROVED
X1	Internal Release	9/1/2021	Damon

WIRE CONNECTION TABLE

GND	B37	A37	GND	GND	A37	B37	GND
PCIE_SSD1_RX3_DN	B36	A36	PCIE_RX7_DN	PCIE_SSD1_TX3_DN	A36	B36	PCIE_TX7_DN
PCIE_SSD1_RX3_DF	B35	A35	PCIE_RX7_DP	PCIE_SSD1_TX3_DP	A35	B35	PCIE_TX7_DP
GND	B34	A34	GND	GND	A34	B34	GND
PCIE_SSD1_RX2_DN	B33	A33	PCIE_RX6_DN	PCIE_SSD1_TX2_DN	A33	B33	PCIE_TX6_DN
PCIE_SSD1_RX2_DF	B32	A32	PCIE_RX6_DP	PCIE_SSD1_TX2_DP	A32	B32	PCIE_TX6_DP
GND	B31	A31	GND	GND	A31	B31	GND
SSD1_FAIL	B30	A30	LED_SSD2_FAIL_N	P3V3_STBY	A30	B30	P3V3_STBY
IFDET_SSD1_N	B29	A29	SSD2_IFDET_N	RST_PCIE_SSD1_N	A29	B29	PE_SSD2_RST_N
SSD1_PRESENT_N	B28	A28	SSD2_PRSNT_N	GND	A28	B28	GND
2C_SSD1_SDA	B27	A27	2C_SSD2_SDA	CLK_100M_PCIE_SSD1_DN	A27	B27	CLK_100M_CPU1_2A_DN
2C_SSD1_SCL	B26	A26	2C_SSD2_SCL	CLK_100M_PCIE_SSD1_DP	A26	B26	CLK_100M_CPU1_2A_DP
GND	B25	A25	GND	GND	A25	B25	GND
PCIE_SSD1_RX1_DN	B24	A24	PCIE_RX5_DN	PCIE_SSD1_TX1_DN	A24	B24	PCIE_TX5_DN
PCIE_SSD1_RX1_DF	B23	A23	PCIE_RX5_DP	PCIE_SSD1_TX1_DP	A23	B23	PCIE_TX5_DP
GND	B22	A22	GND	GND	A22	B22	GND
PCIE_SSD1_RX0_DN	B21	A21	PCIE_RX4_DN	PCIE_SSD1_TX0_DN	A21	B21	PCIE_TX4_DN
PCIE_SSD1_RX0_DF	B20	A20	PCIE_RX4_DP	PCIE_SSD1_TX0_DP	A20	B20	PCIE_TX4_DP
GND	B19	A19	GND	GND	A19	B19	GND
PCIE_SSD0_RX3_DN	B18	A18	PCIE_RX3_DN	PCIE_SSD0_TX3_DN	A18	B18	PCIE_TX3_DN
PCIE_SSD0_RX3_DF	B17	A17	PCIE_RX3_DP	PCIE_SSD0_TX3_DP	A17	B17	PCIE_TX3_DP
GND	B16	A16	GND	GND	A16	B16	GND
PCIE_SSD0_RX2_DN	B15	A15	PCIE_RX2_DN	PCIE_SSD0_TX2_DN	A15	B15	PCIE_TX2_DN
PCIE_SSD0_RX2_DF	B14	A14	PCIE_RX2_DP	PCIE_SSD0_TX2_DP	A14	B14	PCIE_TX2_DP
GND	B13	A13	GND	GND	A13	B13	GND
SSD0_FAIL	B12	A12	LED_SSD1_FAIL_N	P3V3_STBY	A12	B12	P3V3_STBY
IFDET_SSD0_N	B11	A11	SSD1_IFDET_N	RST_PCIE_SSD0_N	A11	B11	PE_SSD1_RST_N
SSD0_PRESENT_N	B10	A10	SSD1_PRSNT_N	GND	A10	B10	GND
2C_SSD0_SDA	B9	A9	2C_SSD1_SDA	CLK_100M_PCIE_SSD0_DN	A9	B9	CLK_100M_CPU1_2B_DN
2C_SSD0_SCL	B8	A8	2C_SSD1_SCL	CLK_100M_PCIE_SSD0_DP	A8	B8	CLK_100M_CPU1_2B_DP
GND	B7	A7	GND	GND	A7	B7	GND
PCIE_SSD0_RX1_DN	B6	A6	PCIE_RX1_DN	PCIE_SSD0_TX1_DN	A6	B6	PCIE_TX1_DN
PCIE_SSD0_RX1_DF	B5	A5	PCIE_RX1_DP	PCIE_SSD0_TX1_DP	A5	B5	PCIE_TX1_DP
GND	B4	A4	GND	GND	A4	B4	GND
PCIE_SSD0_RX0_DN	B3	A3	PCIE_RX0_DN	PCIE_SSD0_TX0_DN	A3	B3	PCIE_TX0_DN
PCIE_SSD0_RX0_DF	B2	A2	PCIE_RX0_DP	PCIE_SSD0_TX0_DP	A2	B2	PCIE_TX0_DP
GND	B1	A1	GND	GND	A1	B1	GND
PIN DEFINITION	P1	P2	PIN DEFINITION	PIN DEFINITION	P1	P2	PIN DEFINITION

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DESCRIPTION  
SlimSAS 8i STR to SlimSAS 8i RA CABLE ASSEMBLY

CUSTOMER PART NUMBER

DRAWN BY Man Hin Wong	CHECKED BY D.Choi	SIZE A	PART NUMBER PT-SAS-SL8-RA-LLL	REV X1
APPROVED BY A. Chui	UNIT Milimeter	SCALE	SHEET Page 2/2	