SAMSUNG ELECTRO-MECHANICS

SPECIFICATION

- **Supplier : Samsung electro-mechanics**
- **Product : Multi-layer Ceramic Capacitor**
- **Samsung P/N : CL31A105KB9LNNC**
- **Descriptiont : CAP, 1㎌, 50V, ±10%, X5R, 1206**

A. Samsung Part Number

CL	31	A	105	K	B	9	L	N	N	C
①	②	③	④	⑤	⑥	⑦	⑧	⑨	⑩	⑪

①	**Series**	Samsung Multi-layer Ceramic Capacitor			
②	**Size**	1206 (inch code)	L: 3.2 ± 0.2 mm	W: 1.6 ± 0.2 mm	
⑧	**Thickness division**	Low profile			
③	**Dielectric**	X5R	**Inner electrode**	Ni	
④	**Capacitance**	1 ㎌	**Termination**	Cu	
⑤	**Capacitance tolerance**	±10 %	**Plating**	Sn 100%	(Pb Free)
⑨	**Product**	Normal			
⑥	**Rated Voltage**	50 V	⑩ **Special**	Reserved for future use	
⑦	**Thickness**	0.9 ± 0.1 mm	⑪ **Packaging**	Cardboard Type, 7" reel	

B. Samsung Reliablility Test and Judgement condition

	Performance	Test condition
Capacitance	Within specified tolerance	1㎑±10% 1.0±0.2Vrms
Tan δ (DF)	0.1 max.	
Insulation Resistance	10,000Mohm or 100Mohm·㎌ Whichever is Smaller	Rated Voltage 60~120 sec.
Appearance	No abnormal exterior appearance	Microscope (×10)
Withstanding Voltage	No dielectric breakdown or mechanical breakdown	250% of the rated voltage
Temperature Characterisitcs	X5R (From -55℃ to 85℃, Capacitance change shoud be within ±15%)	
Adhesive Strength of Termination	No peeling shall be occur on the terminal electrode	500g·F, for 10±1 sec.
Bending Strength	Capacitance change : within ±12.5%	Bending to the limit (1mm) with 1.0mm/sec.
Solderability	More than 75% of terminal surface is to be soldered newly	SnAg3.0Cu0.5 solder 245±5℃, 3±0.3sec. (preheating : 80~120℃ for 10~30sec.)
Resistance to Soldering heat	Capacitance change : within ±7.5% Tan δ, IR : initial spec.	Solder pot : 270±5℃, 10±1sec.

	Performance	Test condition
Vibration Test	Capacitance change : within ±5% Tan δ, IR : initial spec.	Amplitude : 1.5mm From 10㎐ to 55㎐ (return : 1min.) 2hours × 3 direction (x, y, z)
Moisture Resistance	Capacitance change : within ±12.5% Tan δ : 0.125 max IR : 12.5MΩ·μF or Over	With rated voltage 40±2℃, 90~95%RH, 500+12/-0hrs
High Temperature Resistance	Capacitance change : within ±12.5% Tan δ : 0.125 max IR : 25MΩ·μF or Over	With 150% of the rated voltage Max. operating temperature 1000+24/-0hrs
Temperature Cycling	Capacitance change : within ±7.5% Tan δ, IR : initial spec.	1 cycle condition Min. operating temperature → 25℃ → Max. operating temperature → 25℃ 5 cycle test

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5℃, 10sec. Max)

* For the more detail Specification, Please refer to the Samsung MLCC catalogue.