AirPrime EM7421

Product Technical Specification



Rev 4

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Revision History

Revision number	Release date	Changes
1	February 2020	Preliminary draft
2	April 2020	Updated Carrier Aggregation Download Combinations
3	September 2020	Updated CA combinations Added System, Tx, and Rx block diagrams Updated Rx Sensitivity Updated Current Consumption
4	September 2020	Removed A-GPS statement from Position Location (GNSS) topic

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>> 1: Introduction

The Sierra Wireless EM7421 Embedded Module is an M.2 module that provides LTE, UMTS, and GNSS connectivity for notebook, ultrabook, tablet computers, and M2M applications over several radio frequency bands.

Supported RF bands

The module, based on Qualcomm's MDM9250 baseband processor, supports data operation on LTE and UMTS networks over the bands described in Table 1-1, with LTE carrier aggregation (CA) as described in Table 1-2 and Table 1-3.

Table 1-1: Supported RF Bands

RAT							Bands						
RAT.	1	3	5	7	8	20	28	32	38	40	41	42	43
LTE ^a	F	F		F	F	F	F	F	Т	Т	Т	Т	Т
UMTS ^b	Υ		Υ		Y								
GNSS	• G • B	BLONAS eiDou:	75.42 N SS: 160 1561.09 1575.42	2 MHz 98 MHz									

 a. (LTE) Downlink MIMO support (2x2; 4x2) F=FDD; T=TDD Data rates: Downlink (Cat 7 with 2CA, 64QAM=300 Mbps), Uplink (Cat 13 with 2CA contiguous,

64QAM=150 Mbps) b. UMTS (DC-HSPA+, HSPA+, HSPA, UMTS)

Diversity support Data rates: Downlink (Cat 24, up to 42 Mbps), Uplink (Cat 6, up to 5.76 Mbps)

Table 1-2: Carrier Aggregation Downlink Combinations

1 Band/2CC	2 Bands/2CC
	CA_1A-8A
CA_1C	CA_1A-20A
	CA_1A-28A
CA_3A-3A	CA_3A-7A
CA_3C	CA_3A-8A
	CA_3A-20A
	CA_3A-28A
CA_7A-7A	CA_7A-8A
CA_7B	CA_7A-20A
CA_7C	CA_7A-28A

1 Band/2CC	2 Bands/2CC
	CA_20A-32A
CA_38C	
CA_40A-40A	
CA_40C	
CA_41A-41A	
CA_41C	
CA_42A-42A	
CA_42C	
CA_43C	

Table 1-2: Carrier Aggregation Downlink Combinations (Continued)

Table 1-3: Carrier Aggregation Uplink Combinations

CA_1C
CA_3C
CA_7C
CA_41C
CA_42C
CA_43C

Physical Features

- M.2 form factor—WWAN Type 3042-S3-B (in WWAN—USB 3.0 Port Configuration 2), as specified in [7] PCI Express NGFF (M.2) Electromechanical Specification Revision 1.0. (Note: Any variations from the specification are detailed in this document.)
 - Ambient operating temperature range with appropriate heatsinking:
 - Class A (3GPP compliant): -30°C to +70°C
 - Class B (operational, non-3GPP compliant): -40°C to +85°C (reduced operating parameters required)

Important: The internal module temperature (reported by AT!PCTEMP) must be kept below 100°C. For best performance, the internal module temperature should be kept below 85°C. Proper mounting, heat sinks, and active cooling may be required, depending on the integrated application.

Application Interface Features

- USB interface (QMI) for Linux and Android
- MBIM for Windows 10 and up, and Linux
- AT command interface ([1] AT Command Set for User Equipment (UE) (Release 6) (Doc# 3GPP TS 27.007), plus proprietary extended AT commands)
- Software Development Kits (SDK), including API (Application Program Interface) functions:
 - Windows 10
 - Linux
- Support for active antenna control via dedicated antenna control signals (ANTCTL0:3)
- Dynamic power reduction support via software and dedicated GPIO (DPR)
- OMA DM (Open Mobile Alliance Device Management)
- FOTA (Firmware Over The Air)

Note: OMA DM and FOTA support is operator-dependent.

Module Features

- LTE / DC-HSPA+ / HSPA+ / HSPA / UMTS (WCDMA) operation
- Multiple (up to 16) cellular packet data profiles
- Traditional modem COM port support for AT commands
- USB suspend/resume
- Sleep mode for minimum idle power draw
- SIM application tool kit with proactive SIM commands
- Enhanced Operator Name String (EONS)
- Mobile-originated PDP context activation / deactivation
- Support QoS QCI (3GPP Release 12)
- Static and Dynamic IP address. The network may assign a fixed IP address or dynamically assign one using DHCP (Dynamic Host Configuration Protocol).
 PAP and CHAP support
- PDP context type (IPv4, IPv6, or IPv4v6)
- RFC1144 TCP/IP header compression

LTE Features

- Carrier aggregation:
 - · DL LTE-FDD—40 MHz
 - · DL LTE-TDD—40 MHz
 - UL LTE—40 MHz intraband contiguous
- CSG support (LTE Femto)
- LTE Advanced receivers (NLIC, eICIC, feICIC)
- Basic cell selection and system acquisition
 - PSS/SSS/MIB decode
 - SIB1–SIB16 decoding
 - NAS/AS security procedures
 - Snow 3G/AES/ZUC security

- CQI/RI/PMI reporting
- Paging procedures
 - · Paging in Idle and Connected mode
- Dedicated bearer
 - Network-initiated dedicated bearer
 - UE-initiated dedicated bearer
- Multiple PDN connections (IPv4 and IPv6 combinations), subject to operating system support.
- Connected mode intra-LTE mobility
- Idle mode intra-LTE mobility
- iRAT between LTE/3G for idle and connection release with redirection
- Detach procedure
 - · Network-initiated detach with reattach required
 - · Network-initiated detach followed by connection release

Short Message Service (SMS) Features

- Mobile-originated and mobile-terminated SMS over IMS
- Mobile-originated and mobile-terminated SMS over SGs

Position Location (GNSS)

- Customizable tracking session
- Automatic tracking session on startup
- Concurrent standalone GPS, GLONASS, Galileo, and BeiDou
- Assisted GPS/GLONASS SUPL2.0
- gpsOneXTRA 1.0/2.0/3.0/3.1
- GNSS reception on dedicated connector or diversity connector

Supporting Documents

Several additional documents describe module design, usage, integration, and other features. See References on page 62.

Accessories

A hardware development kit is available for AirPrime M.2 modules. The kit contains hardware components for evaluating and developing with the module, including:

- Development board
- Cables
- Antennas
- Other accessories

For over-the-air LTE testing, ensure that suitable antennas are used.

Required Connectors

Table 1-4 describes the connectors used to integrate the EM7421 Embedded Module into your host device.

Connector type	Description
RF cables	 Mate with M.2-spec connectors Three connector jacks (I-PEX 20448-001R-081 or equivalent)
EDGE (67 pin)	 Slot B compatible—Per the M.2 standard ([7] PCI Express NGFF (M.2) Electromechanical Specification Revision 1.0), a generic 75 pin position EDGE connector on the motherboard uses a mechanical key to mate with the 67 pin notched module connector. Manufacturers include LOTES (part #APCI0018-P001A01), Kyocera, JAE, Tyco, and Longwell.
SIM	Industry-standard connector. See SIM Interface on page 24 for details.

Table 1-4: Required Host-Module Connectors^a

a. Manufacturers/part numbers are for reference only and are subject to change. Choose connectors that are appropriate for your own design.

Ordering Information

To order, contact the Sierra Wireless Sales Desk at +1 (604) 232-1488 between 8 AM and 5 PM Pacific Time.

Integration Requirements

Sierra Wireless provides, in the documentation suite, guidelines for successful module integration and offers integration support services as necessary.

When integrating the EM7421 Embedded Module, the following items must be addressed:

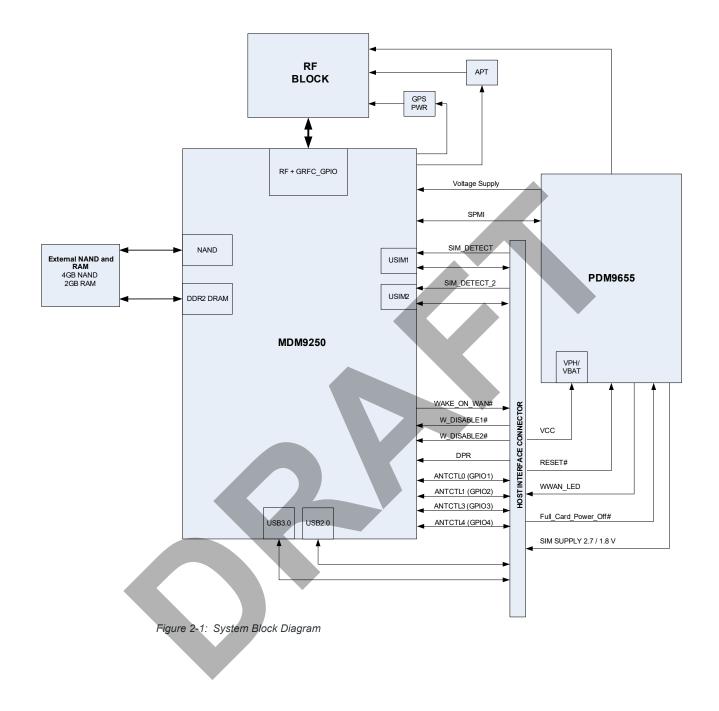
- Mounting—Effect on temperature, shock, and vibration performance
- Power supply—Impact on battery drain and possible RF interference
- Antenna location and type—Impact on RF performance
- **Regulatory approvals**—As discussed in Regulatory Compliance and Industry Certifications on page 53.
- Service provisioning—Manufacturing process
- Software—As discussed in Software Interface on page 49.
- Host interface—Compliance with interface voltage levels

>>> 2: Electrical Specifications

The system block diagram in Figure 2-1 represents the EM7421 module integrated into a host system. The module includes the following interfaces to the host:

- Full_Card_Power_Off#—Input supplied to the module by the host—active-low to turn the unit off, or active-high to turn the unit on.
- W_DISABLE1#—Active low input from the host to the EM7421 disables the main RF radio.
- W_DISABLE2#—Active low input from the host to the EM7421 disables the GNSS radio receiver.
- WAKE_ON_WAN#—Active low output used to wake the host when specific events occur.
- WWAN_LED#—Active-low LED drive signal provides an indication of WAN radio ON state.
- RESET#—Active low input from the host used to reset the module.
- Antenna—Three RF connectors (main (Rx/Tx), GNSS, and auxiliary (diversity/MIMO/ GNSS)). For details, see RF Specifications on page 33.
- Antenna control—Four signals that can be used to control external antenna switches.
- Dynamic power control—Signal used to adjust Tx power to meet FCC SAR requirements. For details, see Tx Power Control on page 48.).
- Dual SIM—Supported through the interface connector. The SIM cavities / connectors must be placed on the host device for this feature.
 - SIM detect—Internal pullup on the module detects whether a SIM is present or not:
 - If a SIM is not inserted, the pin must be shorted to ground.
 - If a SIM is present, the pin will be an open circuit.
- USB—USB 2.0 and USB 3.0 interfaces to the host for data, control, and status information.

The EM7421 has two main interface areas—the host I/O connector and the RF ports. Details of these interfaces are described in the sections that follow.



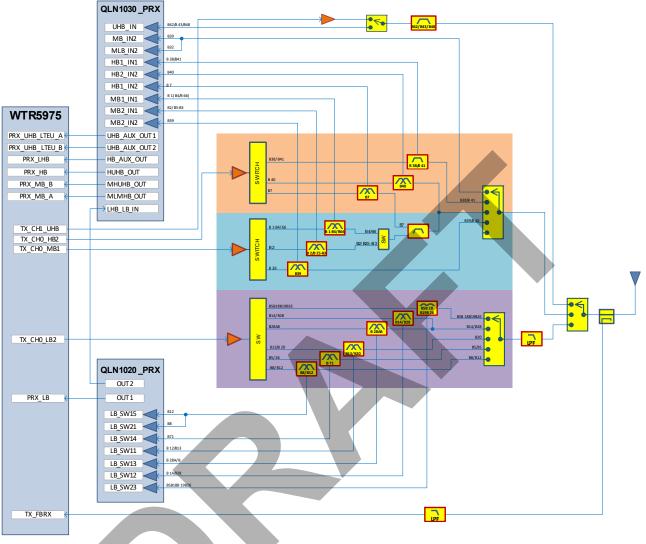


Figure 2-2: Expanded RF (Transmit) Block Diagram

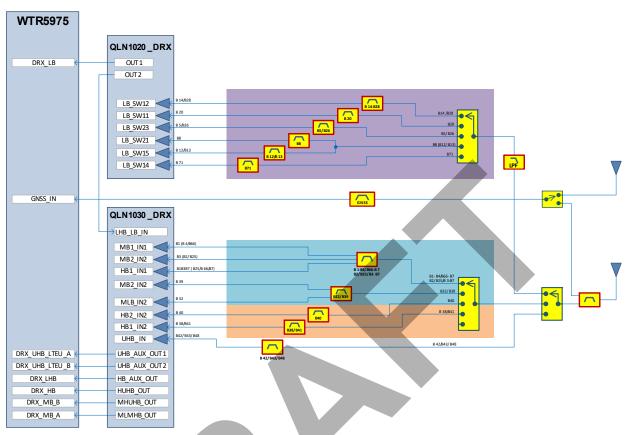


Figure 2-3: Expanded RF (Receive/GNSS) Block Diagram

Host Interface Pin Assignments

The EM7421 host I/O connector provides pins for power, serial communications, and control. Pin assignments are listed in Table 2-1.

Refer to the following tables for pin details based on interface types:

- Table 2-2, Power and Ground Specifications, on page 23
- Table 2-3, USB Interfaces, on page 23
- Table 2-4, SIM Interface Signals, on page 25
- Table 2-5, Module Control Signals, on page 28

Note: On any given interface (USB, SIM, etc.), leave unused inputs and outputs as no-connects.

Note: The host should not drive any signals to the module until >100 ms from the start of the power-on sequence.

Pin	Signal name	Pin	Description	Direction ^c	Active	Voltage levels (V)		
rm	Signal name	type ^b	Description	state		Min	Тур	Max
1	CONFIG_3 (NC in default module configuration)		Reserved—Host must not repurpose this pin.					
2	VCC	V	Power source	Input	Power	3.135	3.7	4.4
3	GND	V	Ground	Input	Power	-	0	-
4	VCC	V	Power source	Input	Power	3.135	3.7	4.4
5	GND	V	Ground	Input	Power	-	0	-
6	Full_Card_Power_Off# ^d	PD	Turn module on	Input	High	0.7	-	4.4
			Turn module off	Input	Low	-0.3	-	0.5
7	USB_D+ ^d	-	USB data positive	Input/Output	Differential	-	-	-
8	W_DISABLE1# ^e	PU	Wireless Disable (main RF radio)	Input	Low	-	-	0.4
				Input	High	0.7	-	4.4
9	USB_D- ^d	-	USB data negative	Input/Output	Differential	-	-	-
10	WWAN_LED#	OC	LED Driver	Output	Low	0	-	0.15
11	GND	V	Ground	Input	Power	-	0	-
12	Кеу	Notch lo	ocation					
13	Кеу	Notch lo	ocation					
14	Кеу	Notch lo	ocation					
15	Кеу	Notch lo	ocation					
16	Кеу	Notch lo	ocation					
17	Кеу	Notch lo	ocation					
18	Кеу	Notch lo	ocation					
19	Кеу	Notch lo	ocation					
20	NC		Reserved—Host must not repurpose this pin.					
21	CONFIG_0 (GND in default module configuration)		Reserved—Host must not repurpose this pin.	Output	-		0	
22	NC		Reserved—Host must not repurpose this pin.					
23	WAKE_ON_WAN# ^d	OC	Wake Host	Output	Low	0		0.1
24	NC		Reserved—Host must not repurpose this pin.					
25	DPR	-	Dynamic power control	Input	High	1.17	1.80	2.10
				Input	Low	-0.3	-	0.63

Table 2-1: Host Interface (75-pin) Connections – Module View^a

Pin	Signal name	Pin	Description	Discotion	Active	Voltage levels (V)		
PIN		type ^b	Description	Direction ^c	state	Min	Тур	Мах
26	W_DISABLE2# ^e	PU	Wireless disable (GNSS radio)	Input	Low	-	-	0.4
				Input	High	0.7	-	4.4
27	GND	V	Ground	Input	Power	-	0	-
28	NC		Reserved—Host must not repurpose this pin.					
29	USB3.0_TX-		USB 3.0 Transmit Data negative	Output	Differential	-	-	-
30	UIM1_RESET ^d	0	SIM Reset	Output	Low	0	-	0.45
					High	2.55 (3V SIM)	3.00 (3V SIM)	3.10 (3V SIM)
						1.35 (1.8V SIM)	1.80 (1.8V SIM)	1.90 (1.8V SIM)
31	USB3.0_TX+		USB 3.0 Transmit Data positive	Output	Differential	-	-	-
32	UIM1_CLK ^d	0	SIM Clock	Output	Low	0	-	0.45
					High	2.55 (3V SIM)	3.00 (3V SIM)	3.10 (3V SIM)
						1.35 (1.8V SIM)	1.80 (1.8V SIM)	1.90 (1.8V SIM)
33	GND	V	Ground	Input	Power	-	0	-
34	UIM1_DATA ^d	-	SIM IO pin	Input	Low	-0.30 (3V SIM)	-	0.60 (3V SIM)
						-0.30 (1.8V SIM)		0.35 (1.8V SIM)
					High	2.10 (3V SIM)	3.00 (3V SIM)	3.30 (3V SIM)
						1.17 (1.8V SIM)	1.80 (1.8V SIM)	2.10 (1.8V SIM)
				Output	Low	0	-	0.40
					High	2.55 (3V SIM)	3.00 (3V SIM)	3.10 (3V SIM)
						1.35 (1.8V SIM)	1.80 (1.8V SIM)	1.90 (1.8V SIM)
35	USB3.0_RX-		USB 3.0 Receive Data negative	Input	Differential	-	-	-
36	UIM1_PWR ^d	V	SIM VCC supply	Output	Power	2.90 (3V SIM)	3.00 (3V SIM)	3.10 (3V SIM)
						1.75 (1.8V SIM)	1.80 (1.8V SIM)	1.85 (1.8V SIM)
37	USB3.0_RX+		USB 3.0 Receive Data positive	Input	Differential	-	-	-
38	NC		Reserved					
39	GND	V	Ground	Input	Power	-	0	-

Pin	Cinnel neme	Pin	Description	D : () ()	Active	Vol	Voltage levels (V)		
Pin Signal name type ^b		Description	Description Direction ^c		Min	Тур	Max		
40	SIM_DETECT_2		SIM2 indication	Input		0 V—SIM n Open circuit	ot present —SIM prese	nt	
41	NC		Reserved						
42	UIM2_DATA ^d	-	SIM2 IO pin	Input	Low	-0.30 (3V SIM) -0.30	-	0.60 (3V SIM) 0.35	
						(1.8V SIM)		(1.8V SIM)	
					High	2.10 (3V SIM)	3.00 (3V SIM)	3.30 (3V SIM)	
						1.17 (1.8V SIM)	1.80 (1.8V SIM)	2.10 (1.8V SIM)	
				Output	Low	0	-	0.40	
					High	2.55 (3V SIM)	3.00 (3V SIM)	3.10 (3V SIM)	
						1.35 (1.8V SIM)	1.80 (1.8V SIM)	1.90 (1.8V SIM)	
43	NC		Reserved						
44	UIM2_CLK ^d	0	SIM2 Clock	Output	Low	0	-	0.45	
					High	2.55 (3V SIM)	3.00 (3V SIM)	3.10 (3V SIM)	
						1.35 (1.8V SIM)	1.80 (1.8V SIM)	1.90 (1.8V SIM)	
45	GND	V	Ground	Input	Power	-	0	-	
46	UIM2_RESET ^d	0	SIM2 Reset	Output	Low	0	-	0.45	
					High	2.55 (3V SIM)	-	3.10 (3V SIM)	
						1.35 (1.8V SIM)		1.90 (1.8V SIM)	
47	NC		Reserved						
48	UIM2_PWR ^d	V	SIM2 VCC supply	Output	Power	2.90 (3V SIM)	3.00 (3V SIM)	3.10 (3V SIM)	
						1.75 (1.8V SIM)	1.80 (1.8V SIM)	1.85 (1.8V SIM)	
49	NC		Reserved						
50	NC		Reserved						
51	GND	V	Ground	Input	Power	-	0	-	
52	NC	OC	Reserved						
53	NC		Reserved						
54	NC	OC	Reserved						
55	NC		Reserved						

Table 2-1: Host Interface (75-pin) Connections — Module View^a (Continued)

Table 2-1:	Host Interface	(75-pin)	Connections — Module	View ^a (Continued)
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Dim	Signal name	Pin	Description	Dina ati a nG	Active	Voltage levels (V)			
Pin		type ^b	Description	Direction ^c	state	Min	Тур	Max	
56	NC		Reserved—Host must not repurpose this pin.						
57	GND	V	Ground	Input	Power	-	0	-	
58	NC		Reserved—Host must not repurpose this pin.						
59	ANTCTL0 (GPIO1)		Customer-defined external switch control	Output	High	1.35	-	1.80	
			for multiple antennas	Output	Low	0	-	0.45	
60	Reserved—Host must not repur	pose this	pin and should leave it n	ot connected.					
61	ANTCTL1 (GPIO2)		Customer-defined external switch control	Output	High	1.35	-	1.80	
			for multiple antennas	Output	Low	0	-	0.45	
62	Reserved—Host must not repur	pose this	pin and should leave it n	ot connected.					
63	ANTCTL2 (GPIO3)		Customer-defined external switch control	Output	High	1.35	-	1.80	
			for multiple antennas	Output	Low	0	-	0.45	
64	Reserved—Host must not repur	pose this	pin and should leave it n	ot connected.					
65	ANTCTL3 (GPIO4)		Customer-defined external switch control	Output	High	1.35	-	1.80	
			for multiple antennas	Output	Low	0	-	0.45	
66	SIM_DETECT ^d	PU	SIM indication	Input			1 not present uit—SIM present		
67	RESET#	PU	Reset module	Input	Low	-0.3		0.63	
68	NC		Reserved						
69	CONFIG_1 (GND in default module configuration)		Reserved—Host must not re-purpose this pin.	Output	-		0		
70	VCC	V	Power source	Input	Power	3.135	3.7	4.4	
71	GND	v	Ground	Input	Power	-	0	-	
72	VCC	V	Power source	Input	Power	3.135	3.7	4.4	
73	GND	V	Ground	Input	Power	-	0	-	
74	VCC	V	Power source	Input	Power	3.135	3.7	4.4	
75	CONFIG_2 (NC in default module configuration)	V	Reserved	Output	-	-		-	

a. All values are preliminary and subject to change.
b. I—Input; O—Digital output; OC—Open Collector output; PU—Digital input (internal pull up); PD—Digital input (internal pull down); V—Power or ground

c. Signal directions are from module's point of view (e.g. 'Output' from module to host, 'Input' to module from host.)d. Required signal

e. Sierra Wireless recommends that the host implement an open collector driver where a Low signal will turn the module off or enter low power mode, and a high signal will turn the module on or leave low power mode.

Power Supply

The host provides power to the EM7421 through multiple power and ground pins as summarized in Table 2-2.

The host must provide safe and continuous power (via battery or a regulated power supply) at all times; the module does not have an independent power supply, or protection circuits to guard against electrical issues.

Name	Pins	Specification	Min	Тур	Max	Units
VCC	2, 4, 70, 72, 74	Voltage range	See Tab	ole 2-1 on	page 19	
(3.7V)	2, 4, 70, 72, 74	Ripple voltage	-	-	100	mV _{pp}
GND	3, 5, 11, 27, 33, 39, 45, 51, 57, 71, 73	-	-	0	-	V

Table 2-2: Power and Ground Specifications

USB Interface

Important: Host support for USB 2.0 or USB 3.0 signals is required.

The device supports USB 2.0 (high speed) and USB 3.0 interfaces for communication between the host and module.

Note: USB 2.0 full speed and low speed are not supported.

The interfaces comply with the [8] Universal Serial Bus Specification, Rev 2.0 and [9] Universal Serial Bus Specification, Rev 3.0 (subject to limitations described below), and the host device must be designed to the same standards.

Table 2-3: USB Interfaces

		Name	Pin	Description
	USB 2.0	USB_D+	7	Data positive
	030 2.0	USB_D-	9	Data negative
		USB3.0-TX- ^a	29	Transmit data negative
	USB 3.0	USB3.0-TX+ ^a	31	Transmit data positive
	036 3.0	USB3.0-RX- ^a	35	Receive data negative
		USB3.0-RX+ ^a	37	Receive data positive

a. Signal directions (Tx/Rx) are from module's point of view.

Host-side Recommendation

Note: When designing the host device, careful PCB layout practices must be followed.

Sierra Wireless recommends the host platform include series capacitors on the USB3.0 Rx signals (no capacitors required for the Tx signals), as shown below.

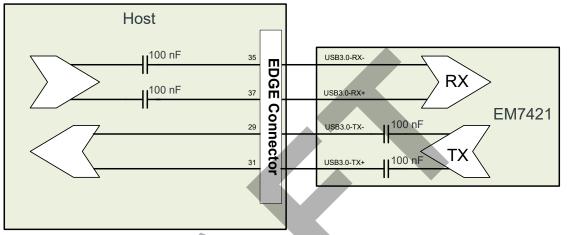


Figure 2-4: Recommended Capacitor Placement for USB3.0 Signals

USB Throughput Performance

This device has been designed to achieve optimal performance and maximum throughput using USB superspeed mode (USB 3.0). Although the device may operate with a high speed host, throughput performance will be on an "as is" basis and needs to be characterized by the OEM. Note that throughput will be reduced and may vary significantly based on packet size, host interface, and firmware revision.

User-developed Drivers

Details for user-developed USB drivers are described in [3] AirCard/AirPrime USB Driver Developer's Guide (Doc# 2130634).

SIM Interface

Note: Host support for SIM interface signals is required.

The module supports up to two SIMs (Subscriber Identity Module) (1.8 V or 3 V). Each SIM holds information for a unique account, allowing users to optimize their use of each account on multiple devices.

The SIM pins (Table 2-4 on page 25) provide the connections necessary to interface to SIM sockets located on the host device as shown in Figure 2-5 on page 26. Voltage levels over this interface comply with 3GPP standards.

The types of SIM connectors used depends on how the host device exposes the SIM sockets.

 Table 2-4:
 SIM Interface Signals

SIM	Name	Pin	Description	SIM contact ^a	Notes
	UIM1_RESET	30	Reset	2	Active low SIM reset
	UIM1_CLK	32	Serial clock	3	Serial clock for SIM data
	UIM1_DATA	34	Data I/O	7	Bi-directional SIM data line
Drimon	UIM1_PWR	36	SIM voltage	1	Power supply for SIM
Primary	SIM_DETECT	66	SIM indication	-	 Input from host indicating whether SIM is present or not Grounded if no SIM is present No-connect (floating) if SIM is inserted
	UIM_GND		Ground	5	Ground reference UIM_GND is common to module ground
	UIM2_RESET	46	Reset	2	Active low SIM reset
	UIM2_CLK	44	Serial clock	3	Serial clock for SIM data
	UIM2_DATA	42	Data I/O	7	Bi-directional SIM data line
Secondary	UIM2_PWR	48	SIM voltage	1	Power supply for SIM
Secondary	SIM_DETECT_2	40	SIM indication	-	 Input from host indicating whether SIM is present or not Grounded if no SIM is present No-connect (floating) if SIM is inserted
	UIM2_GND		SIM indication	-	Ground reference UIM2_GND is common to module ground

a. See Figure 2-6 on page 26 for SIM card contacts.

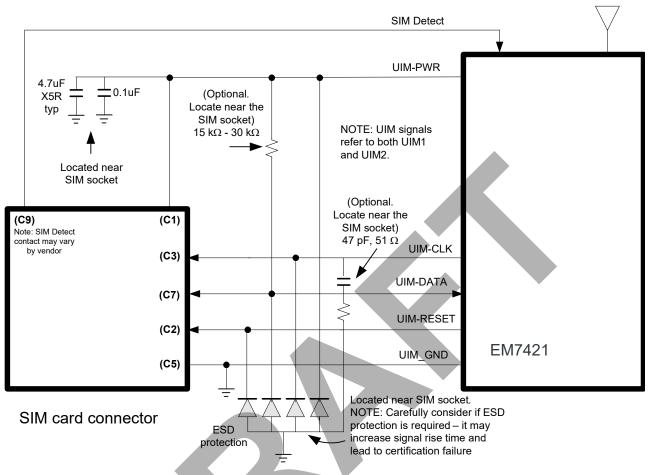


Figure 2-5: SIM Application Interface (applies to both SIM interfaces)

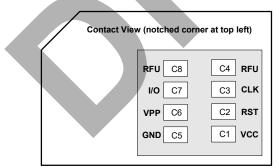


Figure 2-6: SIM Card Contacts (Contact View)

SIM Implementation

Note: For interface design requirements, refer to ETSI TS 102 230 V5.5.0, section 5.2.

When designing the remote SIM interface, you *must* make sure that SIM signal integrity is not compromised.

Some design recommendations include:

- Total impedance of the VCC and GND connections to the SIM, measured at the module connector, should be less than 1 Ω to minimize voltage drop (includes any trace impedance and lumped element components—inductors, filters, etc.).
- Position the SIM connector ≤10 cm from the module. If a longer distance is required because of the host device design, use a shielded wire assembly—connect one end as close as possible to the SIM connector and the other end as close as possible to the module connector. The shielded assembly may help shield the SIM interface from system noise.
- Reduce crosstalk on the UIM1_DATA and UIM2_DATA lines to reduce the risk of failures during GCF approval testing.
- Avoid routing the clock and data lines for each SIM (UIM1_CLK/UIM1_DATA, UIM2_CLK/UIM2_DATA) in parallel over distances >2 cm—cross-coupling of a clock and data line pair can cause failures.
- 3GPP has stringent requirements for I/O rise time (<1 µs), signal level limits, and noise immunity—consider this carefully when developing your PCB layout.
 - Keep signal rise time <1 µs—keep SIM signals as short as possible, and keep very low capacitance traces on the data and clock signals (UIM1_CLK, UIM1_DATA, UIM2_CLK, UIM2_DATA). High capacitance increases signal rise time, potentially causing your device to fail certification tests.
- Add external pull-up resistors (15 kΩ–30 kΩ), if required, between the data and power lines for each SIM (UIM1_DATA/UIM1_PWR, UIM2_DATA/UIM2_PWR) to optimize the signal rise time.
- VCC line should be decoupled close to the SIM socket.
- SIM is specified to run up to 5 MHz (SIM clock rate). Take note of this speed in the placement and routing of the SIM signals and connectors.
- You must decide whether additional ESD protection is required for your product, as it is dependent on the application, mechanical enclosure, and SIM connector design. The SIM pins will require additional ESD protection if they are exposed to high ESD levels (i.e. can be touched by a user).
- Putting optional decoupling capacitors on the SIM power lines (UIM1_PWR, UIM2_PWR) near the SIM sockets is recommended—the longer the trace length (impedance) from the socket to the module, the greater the capacitance requirement to meet compliance tests.
- Putting an optional series capacitor and resistor termination (to ground) on the clock lines (UIM1_CLK, UIM2_CLK) at the SIM sockets to reduce EMI and increase signal integrity is recommended if the trace length between the SIM socket and module is long—47 pF and 50 Ω resistor are recommended.
- Test your first prototype host hardware with a Comprion IT³ SIM test device at a suitable testing facility.

Control Interface (Signals)

The EM7421 provides signals for:

- Waking the host when specific events occur
- Host control of the module's radios
- Host control of module power
- LED driver output

Note: Host support for Full_Card_Power_Off# is required, and support for other signals in Table 2-5 is optional.

These signals are summarized in Table 2-5 and paragraphs that follow.

Table	2-5	Module	Control	Signals
Table	Z - U .	Module	00111101	Orginals

_								
Name	Pin	Description	Type ^a					
Full_Card_Power_Off#	6	On/off signal	PD					
W_DISABLE1#	8	Wireless disable (Main RF)	PU					
WWAN_LED#	10	LED driver	OC					
WAKE_ON_WAN#	23	Wake host	0					
W_DISABLE2#	26	Wireless disable (GNSS)	PU					
RESET#	67	Reset module	PU					

a. O—Digital pin Output; OC—Open Collector output; PD—Digital pin Input, internal pull down; PU—Digital pin Input, internal pull up

WAKE_ON_WAN# — Wake Host

Note: Host support for WAKE_ON_WAN# is optional.

The EM7421 uses WAKE_ON_WAN# to wake the host when specific events occur.

The host must provide a 5 k Ω -100 k Ω pullup resistor that considers total line capacitance (including parasitic capacitance) such that when WAKE_ON_WAN# is deasserted, the line will rise to 3.7 V (Host power rail) in < 100 ns.

See Figure 2-7 on page 29 for a recommended implementation.

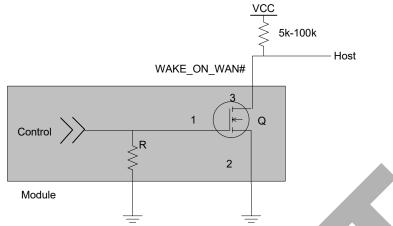


Figure 2-7: Recommended WAKE_ON_WAN# Connection

W_DISABLE1# (Wireless Disable) and W_DISABLE2# (GNSS Disable)

Note: Host support for wireless/GNSS disable signals is optional.

The host device uses W_DISABLE1# to enable/disable the WWAN or radio modem, and W_DISABLE2# to enable/disable GNSS functionality.

Letting these signals float high allows the module to operate normally. These pins have 100 k Ω pull-up resistors. See Figure 2-8 on page 30 for a recommended implementation.

When integrating with your host device, keep the following in mind:

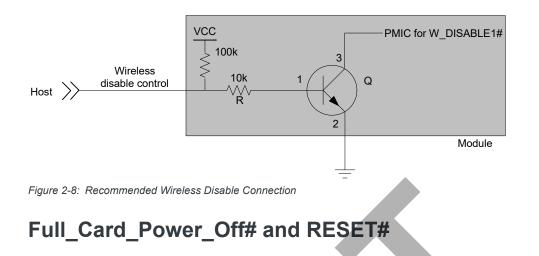
- The signal is an input to the module and should be driven LOW to turn the radio off, or HIGH or floating to keep it on.
- If the host never needs to assert this power state control to the module, leave this signal unconnected from the host interface.

Table 2-6: W_DISABLE1#/W_DISABLE2# Usage

•

Name	Pin	Description/notes
W_DISABLE1#	8	 Enable/disable the WWAN or radio modem^a. When disabled, the modem cannot transmit or receive. Leave as not connected or drive HIGH to keep the modem always on. Drive LOW to turn the modem off.
W_DISABLE2#	26	 Enable/disable GNSS functionality^a Leave as not connected or drive HIGH to enable GNSS functionality. Drive LOW to disable GNSS functionality. For details on enabling / disabling GNSS functionality, see the AT!CUSTOM="GPSENABLE" command.

a. Sierra Wireless recommends that the host implement an open collector driver where a Low signal turns off the modem or disables GNSS functionality, and a high signal turns on the modem or enables GNSS functionality.



Note: Host support for Full_Card_Power_Off# is required, and support for RESET# is optional.

Full_Card_Power_Off# and RESET# are inputs to the module that the host uses as described in Table 2-7.

For timing details, see Power On/Off Timing for the USB on page 46.

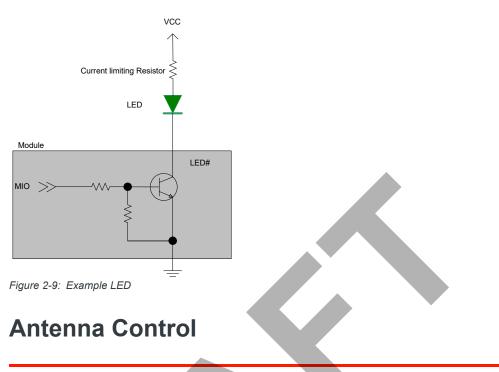
Name	Pin	Description / notes
Full_Card_Power_Off#	6	 Powers the module on/off. Signal is required. Pull HIGH to keep the module on. To keep the module always on: Tie the pin directly to a host GPIO (1.8V), or Use an external pull-up to pull signal high (10–20k for 1.8V, 75–100k for VCC rail). Note that a larger-value resistor will reduce leakage current. To power off the module, see Required Shutdown Sequence on page 47.
RESET#	67	 Resets the module. Signal is optional. The module will operate correctly if the pin is left disconnected on the host. To reset the module, pulse the RESET# pin with a logic low signal for 3 (min) to 5.5 seconds (max)—if the signal is held low for more than 5.5 seconds, the reset cycle restarts, and if it continues to be held low through several cycles, the module will not fully boot. Otherwise, leave the signal floating or high impedance (the module will remain operational because the module has a pull-up resistor to an internal reference voltage (1.8V) in place.). RESET# High

Table 2-7: Full_Card_Power_Off# and RESET# Usage

WWAN_LED#—LED Output

Note: Host support for WWAN_LED# is optional.

The configuration for the LED shown in Figure 2-9 is customizable. Contact your Sierra Wireless account representative for details.



Note: Host support for antenna control signals is optional.

The EM7421 provides four output signals (listed in Table 2-8) that may be used for host designs that incorporate tunable antennas. Customers can configure these signals as appropriate for the operating band(s) using the command AT!ANTSEL.

Note:

- Sierra Wireless recommends that two signals be used for high bands, and the other two signals for low/mid bands.
- To avoid detuning the PCC band, customers must make sure there are no GPIO state conflicts between the PCC and SCC for all supported CA combinations.

Table 2-8: Antenna Control Signals

Name	Pin	Description
ANTCTL0	59	
ANTCTL1	61	Customer-defined external switch controls for tunable
ANTCTL2	63	antennas
ANTCTL3	65	

3: RF Specifications

The EM7421 includes three RF connectors for use with host-supplied antennas:

- Main RF connector—Tx/Rx path
- GNSS RF connector—Dedicated GPS, GLONASS, BeiDou and Galileo
- Auxiliary RF connector—Diversity, MIMO, GPS, GLONASS, BeiDou and Galileo

The module does not have integrated antennas.

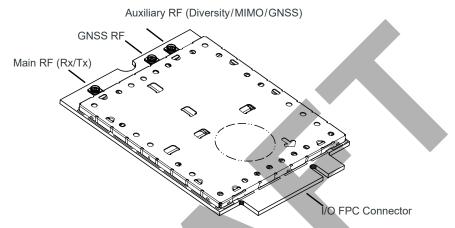


Figure 3-1: Module Connectors (Preliminary shield design, subject to change)

RF Connections

When attaching antennas to the module:

- Use RF plug connectors that are compatible with the following RF receptacle connectors: Foxconn (KK12011-02-7H), Longwell (911-002-0006R), Speedtech (C87P101-00001-H), Murata (MM4829-2702RA4 (HSC)), IPEX (20449-001E (MHF4)).
 - Match coaxial connections between the module and the antenna to 50 Ω .
- Minimize RF cable losses to the antenna; the recommended maximum cable loss for antenna cabling is 0.5 dB.
- To ensure best thermal performance, use the mounting hole (if possible) to attach (ground) the device to a metal chassis.

Note: If the antenna connection is shorted or open, the modem will not sustain permanent damage.

Shielding

The module is fully shielded to protect against EMI and must not be removed.

Antenna and Cabling

When selecting the antenna and cable, it is critical to RF performance to match antenna gain and cable loss.

Note: For detailed electrical performance criteria, see Appendix A: Antenna Specification on page 54.

Choosing the Correct Antenna and Cabling

When matching antennas and cabling:

- The antenna (and associated circuitry) should have a nominal impedance of 50 Ω with a return loss of better than 10 dB across each frequency band of operation.
- The system gain value affects both radiated power and regulatory (FCC, IC, CE, etc.) test results.

Designing Custom Antennas

Consider the following points when designing custom antennas:

- A skilled RF engineer should do the development to ensure that the RF performance is maintained.
- If both UMTS and CDMA modules will be installed in the same platform, you may want to develop separate antennas for maximum performance.

Determining the Antenna's Location

When deciding where to put the antennas:

- Antenna location may affect RF performance. Although the module is shielded to
 prevent interference in most applications, the placement of the antenna is still very
 important—if the host device is insufficiently shielded, high levels of broadband or
 spurious noise can degrade the module's performance.
- Connecting cables between the module and the antenna must have 50 Ω impedance. If the impedance of the module is mismatched, RF performance is reduced significantly.
- Antenna cables should be routed, if possible, away from noise sources (switching power supplies, LCD assemblies, etc.). If the cables are near the noise sources, the noise may be coupled into the RF cable and into the antenna. See Interference from Other Wireless Devices on page 35.

Disabling the Diversity Antenna

Certification testing of a device with an integrated EM7421 may require the module's main and diversity antennas to be tested separately.

To facilitate this testing, receive diversity can be enabled/disabled using AT commands:

- !RXDEN—Used to enable/disable diversity for single-cell call (no carrier aggregation).
- !LTERXCONTROL—Used to enable/disable paths (in carrier aggregation scenarios) after a call is set up.

Important: LTE networks expect modules to have more than one antenna enabled for proper operation. Therefore, customers must not commercially deploy their systems with the diversity antenna disabled.

Note: A diversity antenna is used to improve connection quality and reliability through redundancy. Because two antennas may experience difference interference effects (signal distortion, delay, etc.), when one antenna receives a degraded signal, the other may not be similarly affected.

Ground Connection

When connecting the module to system ground:

- Prevent noise leakage by establishing a very good ground connection to the module through the host connector.
- Connect to system ground using the mounting hole shown in Figure 3-1 on page 33.
- Minimize ground noise leakage into the RF. Depending on the host board design, noise could *potentially* be coupled to the module from the host board. This is mainly an issue for host designs that have signals traveling along the length of the module, or circuitry operating at both ends of the module interconnects.

Interference and Sensitivity

Several interference sources can affect the module's RF performance (RF desense). Common sources include power supply noise and device-generated RF.

RF desense can be addressed through a combination of mitigation techniques (Methods to Mitigate Decreased Rx Performance on page 36) and radiated sensitivity measurement (Radiated Sensitivity Measurement on page 37).

Note: The EM7421 is based on ZIF (Zero Intermediate Frequency) technologies. When performing EMC (Electromagnetic Compatibility) tests, there are no IF (Intermediate Frequency) components from the module to consider.

Interference from Other Wireless Devices

Wireless devices operating inside the host device can cause interference that affects the module.

To determine the most suitable locations for antennas on your host device, evaluate each wireless device's radio system, considering the following:

- Any harmonics, sub-harmonics, or cross-products of signals generated by wireless devices that fall in the module's Rx range may cause spurious response, resulting in decreased Rx performance.
- The Tx power and corresponding broadband noise of other wireless devices may overload or increase the noise floor of the module's receiver, resulting in Rx desense.

The severity of this interference depends on the closeness of the other antennas to the module's antenna. To determine suitable locations for each wireless device's antenna, thoroughly evaluate your host device's design.

Host-generated RF Interference

All electronic computing devices generate RF interference that can negatively affect the receive sensitivity of the module.

Proximity of host electronics to the antenna in wireless devices can contribute to decreased Rx performance. Components that are most likely to cause this include:

- Microprocessor and memory
- Display panel and display drivers
- Switching-mode power supplies

Device-generated RF Interference

The module can cause interference with other devices. Wireless devices such as AirPrime embedded modules transmit in bursts (pulse transients) for set durations (RF burst frequencies). Hearing aids and speakers convert these burst frequencies into audible frequencies, resulting in audible noise.

Methods to Mitigate Decreased Rx Performance

It is important to investigate sources of localized interference early in the design cycle. To reduce the effect of device-generated RF on Rx performance:

- Put the antenna as far as possible from sources of interference. The drawback is that the module may be less convenient to use.
- Shield the host device. The module itself is well shielded to avoid external interference. However, the antenna cannot be shielded for obvious reasons. In most instances, it is necessary to employ shielding on the components of the host device (such as the main processor and parallel bus) that have the highest RF emissions.
- Filter out unwanted high-order harmonic energy by using discrete filtering on low frequency lines.
- Form shielding layers around high-speed clock traces by using multi-layer PCBs.
- Route antenna cables away from noise sources.

Radiated Spurious Emissions (RSE)

When designing an antenna for use with AirPrime embedded modules, the host device with an AirPrime embedded module must satisfy any applicable standards/local regulatory bodies for radiated spurious emission (RSE) for receive-only mode and for transmit mode (transmitter is operating).

Note that antenna impedance affects radiated emissions, which must be compared against the conducted 50 Ω emissions baseline. (AirPrime embedded modules meet the 50 Ω conducted emissions requirement.)

Radiated Sensitivity Measurement

A wireless host device contains many noise sources that contribute to a reduction in Rx performance.

To determine the extent of any receiver performance desensitization due to self-generated noise in the host device, over-the-air (OTA) or radiated testing is required. This testing can be performed by Sierra Wireless or you can use your own OTA test chamber for in-house testing.

Sierra Wireless' Sensitivity Testing and Desensitization Investigation

Although AirPrime embedded modules are designed to meet network operator requirements for receiver performance, they are still susceptible to various performance inhibitors.

As part of the Engineering Services package, Sierra Wireless offers modem OTA sensitivity testing and desensitization (desense) investigation. For more information, contact your account manager or the Sales Desk (see Contact Information on page 3).

Note: Sierra Wireless has the capability to measure TIS (Total Isotropic Sensitivity) and TRP (Total Radiated Power) according to CTIA's published test procedure.

Sensitivity vs. Frequency

For UMTS bands, sensitivity is defined as the input power level in dBm that produces a BER (Bit Error Rate) of 0.1%. Sensitivity should be measured at all UMTS frequencies across each band.

For LTE bands, sensitivity is defined as the RF level at which throughput is 95% of maximum.

Supported Frequencies

The EM7421 supports:

- Multiple-band LTE—See Table 3-1 on page 38 (supported bands) and Table 3-2 on page 38 (LTE bandwidth support).
- LTE Advanced carrier aggregation—See Table 1-2 on page 10 and Table 1-3 on page 11 for details.
- Multiple-band WCDMA/HSPA/HSPA+/DC-HSPA+—See Table 3-3 on page 39.
- Multiple-band WCDMA receive diversity
- GPS, GLONASS, BeiDou, Galileo—See Table 3-7 on page 40.
- Inter-RAT and inter-frequency cell reselection and handover between supported frequency bands

Band	Frequency (Tx)	Frequency (Rx)			
B1	1920–1980 MHz	2110–2170 MHz			
B3	1710–1785 MHz	1805–1880 MHz			
B7	2500–2570 MHz	2620–2690 MHz			
B8	880–915 MHz	925–960 MHz			
B20	832–862 MHz	791–821 MHz			
B28	703–748 MHz	758–803 MHz			
B32	n/a	1452–1496 MHz			
B38	2570–2620 MHz (TDD)				
B40	2300–2400 MHz (TDD)				
B41	2496–2690 MHz (TDD)				
B42	3400–3600 MHz (TDD)				
B43	3600–3800	MHz (TDD)			

Table 3-1: LTE Frequency Bands

Table 3-2: LTE Bandwidth Support^a

Band	1.4 MHz	3 MHz	5 MHz	10 MHz	15 MHz	20 MHz
B1	×	×	K	~	~	 ✓
B3	~	~	~	~	✓ ^b	✓ ^b
B7	×	×	~	~	✓ ^c	✔ ^{b,c}
B8	X	~	~	✓ ^b	×	×
B20	×	×	~	✓ ^b	✓b	✓ ^b
B28	×	~	v	✓ ^b	✓ ^b	✓ ^{b,d}
B32	×	×	~	~	~	v
B38	×	×	~	~	✓ ^c	✓ ^c
B40	×	×	 	~	~	v
B41	×	×	~	~	~	v
B42	×	×	 	~	~	~
B43	×	×	 ✓ 	 	 	 ✓

a. Table contents are derived from 3GPP TS 36.521-1 v12.6.0, table 5.4.2.1-1.

 b. Bandwidth for which a relaxation of the specified UE receiver sensitivity requirement (Clause 7.3 of 3GPP TS 36.521-1 v12.6.0) is allowed.

c. Bandwidth for which uplink transmission bandwidth can be restricted by the network for some channel assignments in FDD/TDD co-existence scenarios in order to meet unwanted emissions requirements (Clause 6.6.3.2 of 3GPP TS 36.521-1 v12.6.0). d. For 20 MHz bandwidth, the minimum requirements are specified for E-UTRA UL carrier frequencies confined to either 713–723 MHz or 728–738 MHz.

Table 3-3: WCDMA Frequency Bands Support

Band	Frequency (Tx)	Frequency (Rx)
Band 1	1920–1980 MHz	2110–2170 MHz
Band 5	824–849 MHz	869–894 MHz
Band 8	880–915 MHz	925–960 MHz

Conducted Rx Sensitivity / Tx Power

Note: Values in the following tables are preliminary, pending transceiver matching/testing.

	LTE bands	Conducted Rx sensitivity (dBm)						
		Primary (Typ) Secondary (Typ		SIMO (Typ)	SIMO ^a (Worst case)			
B1		-97.5	-98	-100	-96.3			
B3		-98	-97.5	-100	-93.3			
B7		-98	-96.5	-99	-94.3			
B8		-99.5	-100	-101.5	-93.3			
B20		-100	-100	-102	-93.3			
B28	Full RB on downlink;	-101	-100	-102	-94.8			
B32	BW: 10 MHz ^b	-97	-97	-99.5	-96.3			
B38		-97.5	-98	-100	-96.3			
B40		-97.5	-98.5	-100	-96.3			
B41		-97.5	-98	-100	-94.3			
B42		-99	-96	-99.5	-95.0			
B43		-97.5	-98	-100	-95.0			

Table 3-4:	Conducted Rx	(Receive)	Sensitivity —	LTE Bands
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a. Per 3GPP specification

 b. Sensitivity values scale with bandwidth: x_MHz_Sensitivity = 10_MHz_Sensitivity - 10*log(10 MHz/x_MHz) Note: Bandwidth support is dependent on firmware version.

		Conducted Rx sensitivity (dBm)						
UMTS bands		Primary (Typical)	Secondary (Typical)	Primary/Secondary (Worst case) ^a				
Band 1		-109.0	-109.5	-106.7				
Band 5	0.1% BER 12.2 kbps	-111.5	-112.0	-104.7				
Band 8			-112.0	-103.7				
Band 8 -111.0 -112.0 -103.7 a. Per 3GPP specification -100.0 -100.0								

Table 3-5: Conducted Rx (Receive) Sensitivity-UMTS Bands

Table 3-6: Conducted Tx (Transmit) Power Tolerances

Bands	Conducted Tx power Notes
LTE	
LTE bands 1, 3	22.5 dBm ± 1 dB
LTE bands 7, 38, 40, 41, 42, 43	22 dBm ± 1 dB
LTE bands 8, 20, 28	23 dBm ± 1 dB
UMTS	
Band 1 (IMT 2100 12.2 kbps)	$23 \text{ dBm} \pm 1 \text{ dB}$
Band 5 (UMTS 850 12.2 kbps)	22 dBm ± 1 dB Connectorized (Class 3)
Band 8 (UMTS 900 12.2 kbps)	23 dBm ± 1 dB

GNSS Specifications

Note: For detailed electrical performance criteria, see Recommended GNSS Antenna Specifications on page 56.

Table 3-7: GNSS Specifications^a

Parameter/feature	Description
Satellite channels	Maximum 30 channels (16 GPS, 14 GLONASS), simultaneous tracking
Protocols	NMEA 0183 V3.0
Acquisition time ^b	Hot start: 1 s Warm start: 29 s Cold start: 32 s
Accuracy	Horizontal: < 2 m (50%); < 5 m (90%) Altitude: < 4 m (50%); < 8 m (90%) Velocity: < 0.2 m/s

Parameter/feature	Description
Sensitivity	Tracking ^c : -160 dBm Acquisition ^d (Assisted): -158 dBm Acquisition (Standalone): -145 dBm
Operational limits	Altitude <6000 m or velocity <100 m/s (Either limit may be exceeded, but not both.)

Table 3-7: GNSS Specifications^a (Continued)

a. Preliminary values, pending validation
b. Acquisition times measured with signal strength = -135 dBm
c. Tracking sensitivity is the lowest GNSS signal level for which the device can still detect an in-view satellite 50% of the time when in sequential tracking mode.
d. Acquisition sensitivity is the lowest GNSS signal level for which the device can still detect an in-view satellite 50% of the time.

The module includes an internal GNSS LNA.

>>> 4: Power

Power Consumption

Power consumption measurements in the tables below are for the EM7421 connected to the host PC via USB.

The module does not have its own power source and depends on the host device for power. For a description of input voltage requirements, see Power Supply on page 23.

escription andby current consu	Bands ^a mption (Sleep mo	Тур	Unit	configuration				
andby current consu	mption (Sleep mo							
		de activa	ited ^b)					
E	LTE bands	1.7	mA	DRX cycle = 8 (2.56 s)				
SPA / WCDMA	UMTS bands	1.8	mA	DRX cycle = 8 (2.56 s)				
andby current consu	mption ^c (Sleep mo	de deac	tivated ^t	?)				
Ē	LTE bands	33	mA	DRX cycle = 8 (2.56 s)				
SPA / WCDMA	UMTS bands	33	mA	DRX cycle = 8 (2.56 s)				
Low Power Mode (LPM)/Offline Mode ^c (Sleep mode activated ^b)								
disabled, but module	is operational	1.5	mA					
ow Power Mode (LPM))/Offline Mode ^c (S	leep mo	de deac	tivated ^b)				
⁻ disabled, but module	is operational	33	mA					
Leakage current								
odule powered off— ull_Card_Power_Off# is upplied	s Low, and VCC is	47	μΑ					
	SPA / WCDMA andby current consu E SPA / WCDMA w Power Mode (LPM disabled, but module w Power Mode (LPM disabled, but module akage current odule powered off— II_Card_Power_Off# is	SPA / WCDMA UMTS bands andby current consumption ^c (Sleep model E LTE bands SPA / WCDMA UMTS bands WPower Mode (LPM) / Offline Mode ^c (S E disabled, but module is operational W Power Mode (LPM) / Offline Mode ^c (S E disabled, but module is operational W Power Mode (LPM) / Offline Mode ^c (S E disabled, but module is operational akage current odule powered off— II_Card_Power_Off# is Low, and VCC is	SPA / WCDMA UMTS bands 1.8 andby current consumption ^c (Sleep mode dead E LTE bands 33 SPA / WCDMA UMTS bands 33 WPower Mode (LPM)/Offline Mode ^c (Sleep mode idisabled, but module is operational 1.5 W Power Mode (LPM)/Offline Mode ^c (Sleep mode idisabled, but module is operational 33 adage current 33 odule powered off— 11.5 II_Card_Power_Off# is Low, and VCC is 47	SPA / WCDMA UMTS bands 1.8 mA andby current consumption ^c (Sleep mode deactivated th E LTE bands 33 mA SPA / WCDMA UMTS bands 33 mA SPA / WCDMA UMTS bands 33 mA W Power Mode (LPM)/Offline Mode ^c (Sleep mode active active active I disabled, but module is operational 1.5 mA W Power Mode (LPM)/Offline Mode ^c (Sleep mode deactive active I disabled, but module is operational 33 mA akage current 33 mA odule powered off— II_Card_Power_Off# is Low, and VCC is 47 µA				

 Table 4-1: Averaged Standby DC Power Consumption

a. For supported bands, see Table 3-1, LTE Frequency Bands, on page 38 and Table 3-3, WCDMA Frequency Bands Support, on page 39.

b. Assumes USB bus is fully suspended during measurements C.

LPM and standby power consumption will increase when LEDs are enabled. To reduce power consumption, configure LEDs to remain off while in standby and LPM modes.

Description	Tx power	Current ^a		Notes
Decemption	ix ponor	Тур	Unit	
		418	mA	CA 300/150 Mbps, 20 MHz+20 MHz BW
	0 dBm	254	mA	CA 100/25 Mbps, 10 MHz+10 MHz BW
LTE		260	mA	150/50 Mbps, 20 MHz BW
	Max Tx power	956	mA	CA 300/150 Mbps, 20 MHz+20 MHz BW
		590	mA	CA 100/25 Mbps, 10 MHz+10 MHz BW
		638	mA	150/50 Mbps, 20 MHz BW
DC-HSPA/HSPA	0 dBm	149	mA	All speeds
DC-NSFA/NSFA	Max Tx power	781	mA	All speeds
Peak current		1.31	A	All LTE/WCDMA bands

Table 4-2: Averaged Call Mode DC Power Consumption

a. Measured at 25°C/nominal 3.7 V voltage

Table 4-3: Miscellaneous DC Power Consumption

		Current/Voltage				
Signal	Description	Min	Тур	Мах	Unit	Notes/configuration
	USB active current		15	20	mA	High speed USB connection, $C_L = 50 \text{ pF}$ on D+ and D- signals
VCC	Inrush current		2.2	2.5	A	 Assumes power supply turn on time > 100µs Dependent on host power supply rise time.
	Maximum current	_		1.5	A	 Across all bands, all temperature ranges 3.7 V supply
CNSS Signal				100	mA	Voltage applied to the GNSS antenna to power electronics inside the antenna
GNSS Signal connector	Active bias on GNSS port	3.05	3.15	3.25	V	(GNSS RF connector in Figure 3-1 on page 33).

Warning: The maximum RF power level allowable on any RF port is +10dBm—damage may occur if this level is exceeded.

Module Power States

The module has five power states, as described in Table 4-4.

Table 4-4: Module Power States

State	Details	Host is powered	USB interface active	Radio enabled
Normal (Default state)	 Module is active Default state. Occurs when VCC is first applied, Full_Card_Power_Off# is deasserted (pulled high), and W_DISABLE# is deasserted Module is capable of placing/receiving calls, or establishing data connections on the wireless network Current consumption is affected by several factors, including: Radio band being used Transmit power Receive gain settings Data rate 	~	~	~
Low power ('Airplane mode')	 Module is active Module enters this state: Under host interface control: Host issues AT+CFUN=0 ([1] AT Command Set for User Equipment (UE) (Release 6) (Doc# 3GPP TS 27.007))), or Host asserts W_DISABLE#, after AT!PCOFFEN=0 has been issued. Automatically, when critical temperature or voltage trigger limits have been reached)) 	~	V	×
Sleep	 Normal state of module between calls or data connections Module cycles between wake (polling the network) and sleep, at network provider- determined interval. 	~	¥a	×
Off	 Host keeps module powered off by asserting Full_Card_Power_Off# (signal pulled low or left floating) Module draws minimal current See Full_Card_Power_Off# and RESET# on page 30 for more information. 	•	Xa	×
Disconnected	• Host power source is disconnected from the module and all voltages associated with the module are at 0 V.	×	Xp	×

a. USB interface is suspendedb. USB interface is disconnected

Power State Transitions

The module uses state machines to monitor supply voltage and operating temperature, and notifies the host when critical threshold limits are exceeded. (See Table 4-5 for trigger details and Figure 4-1 for state machine behavior.)

Power state transitions may occur:

- Automatically, when critical supply voltage or module temperature trigger levels are encountered.
- Under host control, using available AT commands in response to user choices (for example, opting to switch to airplane mode) or operating conditions.

 Table 4-5: Power State Transition Trigger Levels

	Voltage		Temperature ^a		
Transition	Trigger	V	Trigger	°C	Nòtes
Normal to Low Power	VOLT_HI_CRIT	4.4	TEMP_LO_CRIT	-45	RF activity suspended
Normal to Low Power	VOLT_LO_CRIT	3.135	TEMP_HI_CRIT	110	KF activity suspended
Low Power to Normal	VOLT_HI_NORM	4.3	TEMP_LO_NORM	-30	
Low Power to Normal or Remain in Normal (Remove warnings)	VOLT_LO_NORM	3.3	TEMP_HI_NORM	70	RF activity resumed
Normal (Issue warning)	VOLT_LO_WARN	3.2	TEMP_HI_WARN	85	• In the TEMP_HI_WARN state, the module may have reduced performance (Class B temperature range).
Power off/on (Host-initiated)	-	-	-	-	 Power off recommended when supply voltage or module operating temperature is critically low or high.

a. Module-reported temperatures at the printed circuit board.

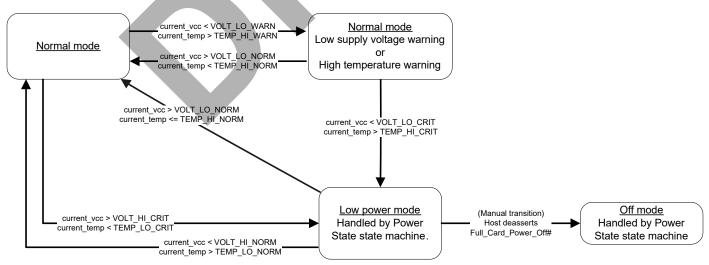


Figure 4-1: Voltage/Temperature Monitoring State Machines

Power Interface

Power Ramp-up

On initial power up, inrush current depends on the power supply rise time—turn on time >100 μ s is required for < 3A inrush current.

The supply voltage must remain within specified tolerances while this is occurring.

Timing

Power On/Off Timing for the USB

Figure 4-2 describes the timing sequence for powering the module on and off.

Note: Before reaching the "Active" state, signals on the host port are considered to be undefined and signal transitions may occur. This undefined state also applies when the module is in reset mode, during a firmware update, or during the Power-off sequence. The host must consider these undefined signal activities when designing the module interface.

Note: The host should not drive any signals to the module until >100 ms from the start of the power-on sequence.

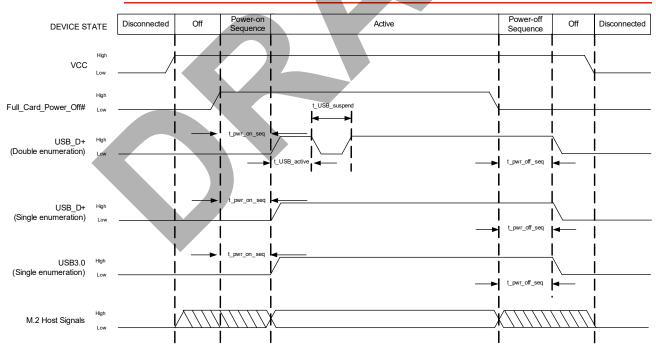


Figure 4-2: Signal Timing (Full_Card_Power_Off#, and USB Enumeration)

		-	
Parameter	Typical (s)	Maximum (s)	
t_pwr_on_seq	0.8	0.9	
t_USB_active	0.12	0.2	
t_USB_suspend	8.5	11	
t_pwr_off_seq	21.4	25	

 Table 4-6:
 USB 2.0
 Power-On / Off Timing Parameters (Double Enumeration)

Table 4-7: USB 2.0 Power-On/Off Timing Parameters (S	ingle Enumeration)

Parameter	Typical (s)	Maximum (s)
t_pwr_on_seq	8.7	11
t_pwr_off_seq	21.4	25

Table 4-8: USB 3.0 Power-On/Off Tim	na Parameters (Single Enumeration)
	ing Falameters (Single Linumeration)

Parameter	Typical (s)	Maximum (s)	
t_pwr_on_seq	8.6	11	
t_pwr_off_seq	21.5	25	

USB Enumeration

The unit supports single and double USB enumeration with the host:

- Single enumeration:
 - Enumeration starts within maximum t_pwr_on_seq seconds of power-on.
- Double enumeration—As shown in Figure 4-2:
 - First enumeration starts within t_pwr_on_seq seconds of power-on (while USB_D+ is high)
 - Second enumeration starts after t_USB_suspend (when USB_D+ goes high again)

Reset Timing

To reset the module, refer to Table 2-7 on page 31 for RESET# signal usage instructions.

Required Shutdown Sequence

Warning: To avoid causing issues with the file system, follow this shutdown sequence.

- 1. Drive Full_Card_Power_Off# low.
- 2. Wait for at least t_pwr_off_seq seconds.
- 3. Remove power.

Power Supply Noise

Noise in the power supply can lead to noise in the RF signal.

The power supply ripple limit for the module is no more than 100 mVp-p 1 Hz to 100 kHz. This limit includes voltage ripple due to transmitter burst activity.

Additional decoupling capacitors can be added to the main VCC line to filter noise into the device.

SED (Smart Error Detection)

The module uses a form of SED to track premature modem resets.

- Module tracks consecutive resets occurring soon after power-on.
- After a sixth consecutive reset, the module waits in boot-and-hold mode for a firmware download to resolve the power-cycle problem.

Tx Power Control

The module's Tx power limit may be controlled using either SAR backoff AT commands or the DPR (Dynamic power control) signal. Use the GPIOSARENABLE parameter for !CUSTOM to choose the method:

- AT commands:
 - !SARSTATEDFLT—Set (or report) the default SAR backoff state that the device uses when it powers up. This setting is persistent across power cycles and overrides any PRI setting.
 - ISARSTATE—Set (or report) the current SAR backoff state (override the default state). This change in state is non-persistent across power cycles.
 - ISARBACKOFF—Set (or report) the maximum Tx power limit for a specific band/ technology/state combination.

Note: A customization is available to invert the DPR logic. (e.g. make DPR low = No SAR backoff)

• Dynamic power control— The module's firmware monitors DPR (pin 25) and adjusts the RF Tx power appropriately, as detailed in Table 4-9. (This state change is equivalent to issuing the !SARSTATE AT command.)

T	able	4-9:	Dyna	amic	Power	Control	of	SAR	Backoff	State
			- J			00111101	••••	0 /	Buokon	0.00

DPR	SAR backoff state
High ^a	No SAR backof
Low	Backoff 1

a. DPR is pulled high by default.

Note: The host can implement an open collector drive for the DPR pin (if a 1.8 V-compatible drive is not available).

>> 5: Software Interface

Support Tools

The EM7421 is compatible with the following support tools from Sierra Wireless and authorized third parties:

- Firmware update utilities from Sierra Wireless
- Sierra Wireless Logger
- QXDM from QUALCOMM
- QUALCOMM Product Support Tool (QPST)
- Windows and Linux SDKs (including API and drivers)

Host Interface

The device supports the following protocols for modem communication:

- MBIM (Mobile Broadband Interface Model)
- Qualcomm QMI interface. (Please contact your Sierra Wireless account representative for QMI interface documentation.)

6: Mechanical and Environmental Specifications

The EM7421 module complies with the mechanical and environmental specifications in Table 6-1. Final product conformance to these specifications depends on the OEM device implementation.

	Mode	Details
Ambient temperature	Operational Class A	-30°C to +70°C – 3GPP compliant
	Operational Class B	-40°C to +85°C, with appropriate heatsinking—non-3GPP compliant (reduced operating parameters required)
	Storage	-40°C to +85°C
High temperature	Operational	85°C, 45 minutes transmission/15 minutes idle, 480 hours
Low temperature	Operational	-40°C, 30 minutes off/5 minutes idle, 120 hours
Relative humidity	Operational	85°C, 85% relative humidity for 240 hours (non-condensing)
Thermal shock	Non-operational	-40°C to 85°C, <30 seconds transition, 10 minutes dwell, 300 cycles
Vibration	Non-operational	Tri-axial vibration, 20 to 5000 Hz, 20 Grms, 10 minutes dwell
Shock	Non-operational	Half sine shock, 6 ms, 30 g, 3x each axis
Drop	Non-operational	1 m unprotected drop on each of six faces (module only)
(Electrostatic discharge (See Electrostatic	Operational	The RF port (antenna launch and RF connector) complies with the IEC 61000-4-2 standard:
Discharge (ESD) on page 51.)		 Electrostatic Discharge Immunity: Test: Level3 Air Discharge: ±8 kV
	Non-operational	 The host connector interface complies with the following standard only: minimum ±500 V Human Body Model (JESD22-A114-B)
Thermal considerations		See Thermal Considerations on page 51.
Form factor		M.2 Form Factor
Dimensions		Length: 42 mm Width: 30 mm Thickness: 2.3 mm
		Weight: 6.6 g

Table 6	-1:	Mechanical	and	Environmental	Specifications ^a
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a. Specifications and associated standards to be confirmed.

Electrostatic Discharge (ESD)

The OEM is responsible for ensuring that the EM7421 host interface pins are not exposed to ESD during handling or normal operation. (See Table 6-1 on page 50 for specifications.)

ESD protection is highly recommended for the SIM connector at the point where the contacts are exposed, and for any other signals from the host interface that would be subjected to ESD by the user of the product. (The device includes ESD protection on the antenna.)

Thermal Considerations

Embedded modules can generate significant amounts of heat that must be dissipated in the host device for safety and performance reasons.

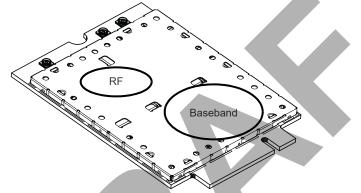


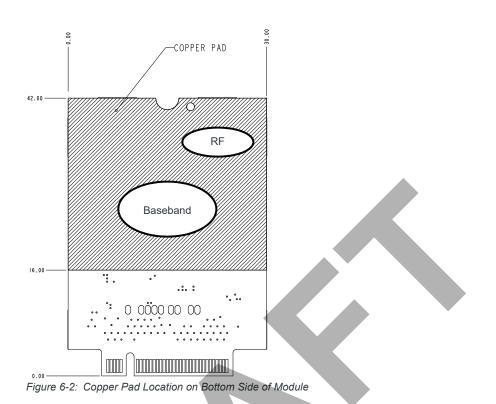
Figure 6-1: Shield Locations (Top View)

The amount of thermal dissipation required depends on:

- Supply voltage—Maximum power dissipation for the module can be up to 3.5 W at voltage supply limits.
- Usage—Typical power dissipation values depend on the location within the host, amount of data transferred, etc.

Specific areas requiring heat dissipation are shown in Figure 6-2:

- RF—Bottom face of module near RF connectors. Likely to be the hottest area.
- Baseband—Bottom face of module, below the baseband area.



To enhance heat dissipation:

- It is recommended to add a heat sink that mounts the module to the main PCB or metal chassis (a thermal compound or pads must be used between the module and the heat sink).
- Maximize airflow over/around the module.
- Locate the module away from other hot components.
- Module mounting holes must be used to attach (ground) the device to the main PCB ground or a metal chassis.
- You may also need active cooling to pull heat away from the module.

Note: Adequate dissipation of heat is necessary to ensure that the module functions properly.

Module Integration Testing

When testing your integration design:

- Test to your worst case operating environment conditions (temperature and voltage)
- Test using worst case operation (transmitter on 100% duty cycle, maximum power)
- Monitor temperature at all shield locations. Attach thermocouples to the areas indicated in Figure 6-1 on page 51 (RF, Baseband).

Note: Make sure that your system design provides sufficient cooling for the module.

(For acceptance, certification, quality, and production (including RF) test suggestions, see Testing on page 67.)

7: Regulatory Compliance and Industry Certifications

This module is designed to meet, and upon commercial release, will meet the requirements of the following regulatory bodies and regulations, where applicable:

- The National Communications Commission (NCC) of Taiwan, Republic of China
- Radio Equipment Directive (RED) of the European Union

The EM7421 Embedded Module complies with the mandatory requirements described in the following standards. The exact set of requirements supported is network operator-dependent.

Table 7-1: Standards Compliance

Technology	Standards	
LTE	• 3GPP Release 11 ^a	
UMTS	• 3GPP Release 9	

a. Some auxiliary functions support Release 12 or Release 13.

Upon commercial release, the following industry certifications will have been obtained, where applicable:

• GCF

Additional certifications and details on specific country approvals may be obtained upon customer request—contact your Sierra Wireless account representative for details.

Additional testing and certification may be required for the end product with an embedded EM7421 module and are the responsibility of the OEM. Sierra Wireless offers professional services-based assistance to OEMs with the testing and certification process, if required.

>>> A: Antenna Specification

This appendix describes recommended electrical performance criteria for main path, diversity path, and GNSS antennas used with AirPrime embedded modules.

The performance specifications described in this section are valid while antennas are mounted in the host device with antenna feed cables routed in their final application configuration.

Note: Antennas should be designed **before** the industrial design is finished to make sure that the best antennas can be developed

Recommended Main/Diversity Antenna Specifications

Table A-1: Antenna Requirements ^a

Parameter	Requirements	Comments
Antenna system	(LTE) External multi-band 2x2 MIMO antenna system (Ant1/ Ant2) ^b (3G) External multi-band antenna system with diversity (Ant1/Ant2) ^c	If Ant2 includes GNSS, then it must also satisfy requirements in Table A-2 on page 56.
Operating bands — Antenna 1	All supporting Tx and Rx frequency bands.	
Operating bands — Antenna 2	All supporting Rx frequency bands, plus GNSS frequency bands if Antenna 2 is used in shared Diversity/MIMO/GNSS mode.	
VSWR of Ant1 and Ant2	< 2:1 (recommended)< 3:1 (worst case)	On all bands including band edges
Total radiated efficiency of Ant1 and Ant2	> 50% on all bands	 Measured at the RF connector. Includes mismatch losses, losses in the matching circuit, and antenna losses, excluding cable loss. Sierra Wireless recommends using antenna efficiency as the primary parameter for evaluating the antenna system. Peak gain is not a good indication of antenna performance when integrated with a host device (the antenna does not provide omni-directional gain patterns). Peak gain can be affected by antenna size, location, design type, etc.—the antenna gain patterns remain fixed unless one or more of these parameters change.

Parameter	Requirements	Comments
Radiation patterns of Ant1 and Ant2	Nominally Omni-directional radiation pattern in azimuth plane.	
Envelope correlation coefficient between Ant1 and Ant2	 < 0.5 on Rx bands below 960 MHz < 0.2 on Rx bands above 1.4 GHz 	
Mean Effective Gain of Ant1 and Ant2 (MEG1, MEG2)	≥ -3 dBi	
Ant1 and Ant2 Mean Effective Gain Imbalance I MEG1 / MEG2 I	< 2 dB for MIMO operation < 6 dB for diversity operation	
Isolation between Ant1 and Ant2 (S21)	> 10 dB	 If antennas can be moved, test all positions for both antennas. Make sure all other wireless devices (Bluetooth or WLAN antennas, etc.) are turned OFF to avoid interference.
Power handling	> 1 W on high bands	 Measure power endurance over 4 hours (estimated talk time) using a 1 W CW signal—set the CW test signal frequency to the middle of each supporting Tx band. Visually inspect device to ensure there is no damage to the antenna structure and matching components. VSWR/TIS/TRP measurements taken before and after this test must show similar results.

Table A-1:	Antenna	Requirements	(Continued) ^a
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a. These worst-case VSWR figures for the transmitter bands may not guarantee RSE levels to be within regulatory limits. The device alone meets all regulatory emissions limits when tested into a cabled (conducted) 50 ohm system. With antenna designs with up to 2.5:1 VSWR or worse, the radiated emissions could exceed limits. The antenna system may need to be tuned in order to meet the RSE limits as the complex match between the module and antenna can cause unwanted levels of emissions. Tuning may include antenna pattern changes, phase/delay adjustment, passive component matching. Examples of the application test limits would be included in FCC Part 22, Part 24 and Part 27,test case 4.2.2 for WCDMA (ETSI EN 301 908-1), where applicable.

- b. Ant1—Primary, Ant2—Secondary (Diversity/MIMO/GNSS)
 c. Ant1—Primary, Ant2—Secondary (Diversity/GNSS)

Recommended GNSS Antenna Specifications

Table A-2:	GNSS	Antenna	Requirements
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Parameter	Requirements	Comments
Frequency range	 Wide-band GNSS: 1559–1606 MHz recommended Narrow-band GPS: 1575.42 MHz ±2 MHz minimum Narrow-band Galileo: 1575.42 MHz ±2 MHz minimum Narrow-band BeiDou: 1561.098 MHz ±2 MHz minimum Narrow-band GLONASS: 1601.72 MHz ±4.2 MHz minimum 	
Field of view (FOV)	 Omni-directional in azimuth -45° to +90° in elevation 	
Polarization (average Gv/Gh)	> 0 dB	Vertical linear polarization is sufficient.
Free space average gain (Gv+Gh) over FOV	> -6 dBi (preferably > -3 dBi)	Gv and Gh are measured and averaged over -45° to +90° in elevation, and ±180° in azimuth.
Gain	 Maximum gain and uniform coverage in the high elevation angle and zenith. Gain in azimuth plane is not desired. 	
Average 3D gain	> -5 dBi	
Isolation between GNSS and Ant1	 All uplink bands: > 10 dB To mitigate GNSS and LTE B13/B14 co-existence: > 20 dB 	
Typical VSWR	< 2.5:1	
Polarization	Any other than LHCP (left-hand circular polarized) is acceptable.	

Antenna Tests

The following guidelines apply to the requirements described in Table A-1 on page 54 and Table A-2 on page 56:

- Perform electrical measurements at room temperature (+20°C to +26°C) unless otherwise specified
- For main and diversity path antennas, make sure the antennas (including contact device, coaxial cable, connectors, and matching circuit with no more than six components, if required) have nominal impedances of 50 Ω across supported frequency bands.
- All tests (except isolation/correlation coefficient)—Test the main or diversity antenna with the other antenna terminated.

- Any metallic part of the antenna system that is exposed to the outside environment needs to meet the electrostatic discharge tests per IEC61000-4-2 (conducted discharge +8kV).
- The functional requirements of the antenna system are tested and verified while the embedded module's antenna is integrated in the host device.

Note: Additional testing, including active performance tests, mechanical, and accelerated life tests can be discussed with Sierra Wireless' engineering services. Contact your Sierra Wireless representative for assistance.



>>> B: Design Checklist

This chapter provides a summary of the design considerations mentioned throughout this guide. This includes items relating to the power interface, RF integration, thermal considerations, cabling issues, and so on.

Note: This is NOT an exhaustive list of design considerations. It is expected that you will employ good design practices and engineering principles in your integration.

Table B-1: Hardware Integration Design Considerations

Suggestion	Section where discussed
Component placement	
If an ESD suppressor is not used on the host device, allow space on the SIM connector for series resistors in layout. (Up to 100 Ω may be used depending on ESD testing requirements).	SIM Implementation on page 27
Minimize RF cable losses as these affect performance values listed in product specification documents.	RF Connections on page 33
Antennas	
Match the module/antenna coax connections to 50 Ω —mismatched antenna impedance and cable loss negatively affect RF performance.	RF Connections on page 33
If installing UMTS and CDMA modules in the same device, consider using separate antennas for maximum performance.	Antenna and Cabling on page 34
Power	
Make sure the power supply can handle the maximum current specified for the module type.	Power Consumption on page 42
Limit the total impedance of VCC and GND connections to the SIM at the connector to less than 1 Ω (including any trace impedance and lumped element components—inductors, filters, etc.). All other lines must have a trace impedance less than 2 Ω .	SIM Implementation on page 27
Decouple the VCC line close to the SIM socket. The longer the trace length (impedance) from socket to module, the greater the capacitance requirement to meet compliance tests.	SIM Implementation on page 27
PCB signal routing	-
USB 2.0/3.0—Route these signals over 90 Ω differential lines on the PCB.	
I2C port—If supported, route these signals away from noise-sensitive signals on the PCB.	
PCM port—If supported, route these signals away from noise-sensitive signals on the PCB.	
EMI/ESD	
Investigate sources of localized interference early in the design cycle.	Methods to Mitigate Decreased Rx Performance on page 36

Suggestion	Section where discussed
If there is any potential ESD exposure to the primary antenna port, add 39 nH shunt induction to Ground as close as possible to the external connection.	
Provide ESD protection for the SIM connector at the exposed contact point (in particular, the CLK, VCC, IO, and RESET# lines).	SIM Implementation on page 27
Keep very low capacitance traces on the UIM_DATA and UIM_CLK signals.	SIM Implementation on page 27
To minimize noise leakage, establish a very good ground connection between the module and host.	Ground Connection on page 35
Route cables away from noise sources (for example, power supplies, LCD assemblies, etc.).	Methods to Mitigate Decreased Rx Performance on page 36
Shield high RF-emitting components of the host device (for example, main processor, parallel bus, etc.).	Methods to Mitigate Decreased Rx Performance on page 36
Use discrete filtering on low frequency lines to filter out unwanted high-order harmonic energy.	Methods to Mitigate Decreased Rx Performance on page 36
Use multi-layer PCBs to form shielding layers around high-speed clock traces.	Methods to Mitigate Decreased Rx Performance on page 36
Thermal	
Test to worst case operating conditions—temperature, voltage, and operation mode (transmitter on 100% duty cycle, maximum power).	Thermal Considerations on page 51
Use appropriate techniques to reduce module temperatures (for example, airflow, heat sinks, heat-relief tape, module placement, etc.).	Thermal Considerations on page 51
Host/Modem communication	
Make sure the host USB driver supports remote wakeup, resume, and suspend operations, and serial port emulation.	[3] AirCard/AirPrime USB Driver Developer's Guide (Doc# 2130634)
When no valid data is being sent, do not send SOF tokens from the host (causes	[3] AirCard/AirPrime USB Driver Developer's Guide

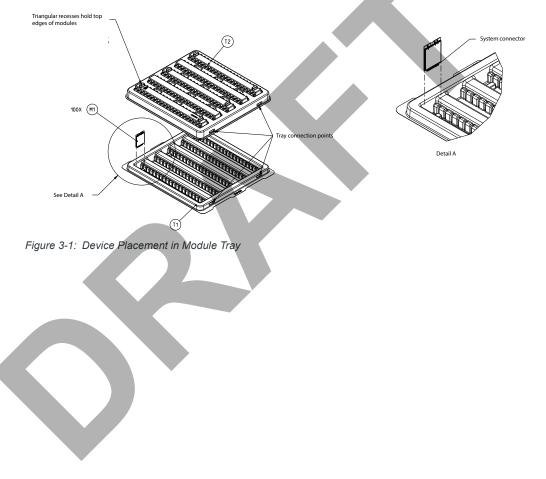
Table B-1: Hardware Integration Design Considerations (Continued)

C: Packaging

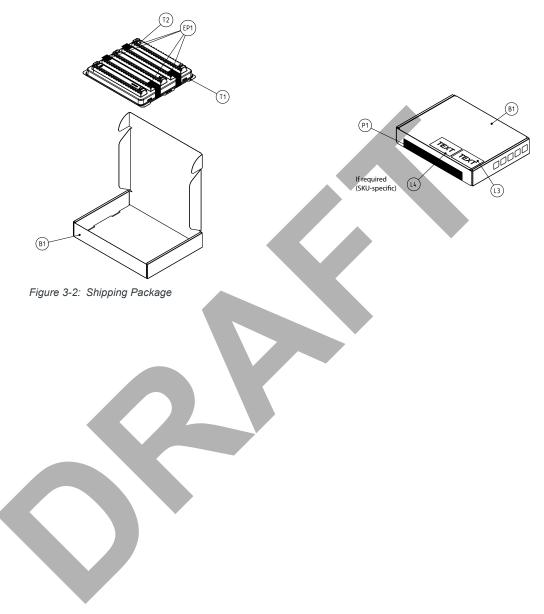
Sierra Wireless AirPrime Embedded Modules are shipped in sealed boxes. The standard packaging (see Figure 3-1), contains a single tray with a capacity of 100 modules. (Note that some SKUs may have custom packaging—contact Sierra Wireless for SKU-specific details.)

In the standard packaging, Embedded Modules are inserted, system connector first, into the bottom portion (T1) of a two-part tray. all facing the same direction. This allows the top edge of each Embedded Module to contact the top of the triangular features in the top portion (T2) of the tray (see Detail A).

The top and bottom portions of the tray snap together at the four connection points.



The tray cover is secured to the tray base with ESD-safe tape (EP1) at the locations indicated. The tray is placed in a manufacturing box(B1), sealed with a security tape (P1), a manufacturing label (L3) is placed on the bottom-right corner, above the security tape, and if required a label (L4) is applied beside the manufacturing label. (See Figure 3-2.)



>>>D: References

This guide deals specifically with hardware integration issues that are unique to AirPrime embedded modules.

Sierra Wireless Documents

The Sierra Wireless documents listed below are available from www.sierrawireless.com. For additional documents describing embedded module design, usage, and integration issues, contact your Sierra Wireless account representative.

Command Documents

[1] AT Command Set for User Equipment (UE) (Release 6) (Doc# 3GPP TS 27.007)

Other Sierra Documents

- [2] M.2 Dev Kit Welcome Letter (Doc# 2400323)
- [3] AirCard/AirPrime USB Driver Developer's Guide (Doc# 2130634)

Industry/Other Documents

The following non-Sierra Wireless references are not included in your documentation package:

- [4] FCC Regulations Part 15 Radio Frequency Devices
- [5] IEC-61000-4-2 level 3 (Electrostatic Discharge Immunity Test)
- [6] Mobile Station (MS) Conformance Specification; Part 4: Subscriber Interface Module (Doc# 3GPP TS 11.10-4)
- [7] PCI Express NGFF (M.2) Electromechanical Specification Revision 1.0
- [8] Universal Serial Bus Specification, Rev 2.0
- [9] Universal Serial Bus Specification, Rev 3.0

[10] JESD22-A114-B

- [11] JESD22-C101
- [12]MIPI Alliance Specification for RF Front-End Control Interface

E: Acronyms

Table E-1: Acronyms and Definitions

Acronym or term	Definition
3GPP	3rd Generation Partnership Project
8PSK	Octagonal Phase Shift Keying
AGC	Automatic Gain Control
A-GPS	Assisted GPS
API	Application Programming Interface
BeiDou	BeiDou Navigation Satellite System A Chinese system that uses a series of satellites in geostationary and middle earth orbits to provide navigational data.
BER	Bit Error Rate—A measure of receive sensitivity
BLER	Block Error Rate
bluetooth	Wireless protocol for data exchange over short distances
CQI	Channel Quality Indication
COM	Communication port
CS	Circuit-switched
CSG	Closed Subscriber Group
CW	Continuous waveform
dB	Decibel = $10 \times \log_{10} (P1/P2)$ <i>P1 is calculated power; P2 is reference power</i> Decibel = $20 \times \log_{10} (V1/V2)$ <i>V1 is calculated voltage, V2 is reference voltage</i>
dBm	A logarithmic (base 10) measure of relative power (dB for decibels); relative to milliwatts (m). A dBm value will be 30 units (1000 times) larger (less negative) than a dBW value, because of the difference in scale (milliwatts vs. watts).
DC-HSPA+	Dual Carrier HSPA+
DCS	Digital Cellular System A cellular communication infrastructure that uses the 1.8 GHz radio spectrum.
DL	Downlink (network to mobile)
DRX	Discontinuous Reception
DSM	Distributed Shared Memory
DUT	Device Under Test
elCIC	Enhanced Inter-Cell Interference Coordination
EIRP	Effective (or Equivalent) Isotropic Radiated Power

Acronym or term	Definition
EMC	Electromagnetic Compatibility
EMI	Electromagnetic Interference
ERP	Effective Radiated Power
ESD	Electrostatic Discharge
FCC	Federal Communications Commission The U.S. federal agency that is responsible for interstate and foreign communications. The FCC regulates commercial and private radio spectrum management, sets rates for communications services, determines standards for equipment, and controls broadcast licensing. Consult www.fcc.gov.
FDD	Frequency Division Duplexing
FDMA	Frequency Division Multiple Access
felCIC	Further Enhanced Inter-Cell Interference Coordination
FER	Frame Error Rate—A measure of receive sensitivity.
firmware	Software stored in ROM or EEPROM; essential programs that remain even when the system is turned off. Firmware is easier to change than hardware but more permanent than software stored on disk.
FOTA	Firmware Over The Air—Technology used to download firmware upgrades directly from the service provider, over the air.
FOV	Field Of View
FSN	Factory Serial Number—A unique serial number assigned to the mini card during manufacturing.
Galileo	A European system that uses a series of satellites in middle earth orbit to provide navigational data.
GCF	Global Certification Forum
GLONASS	Global Navigation Satellite System—A Russian system that uses a series of 24 satellites in middle circular orbit to provide navigational data.
GMSK	Gaussian Minimum Shift Keying modulation
GNSS	Global Navigation Satellite Systems (GPS, GLONASS, BeiDou, and Galileo)
GPS	Global Positioning System An American system that uses a series of 24 satellites in middle circular orbit to provide navigational data.
Host	The device into which an embedded module is integrated
HSDPA	High Speed Downlink Packet Access
HSPA+	Enhanced HSPA, as defined in 3GPP Release 7 and beyond
HSUPA	High Speed Uplink Packet Access
Hz	Hertz = 1 cycle/second

Table E-1: Acronyms and Definitions (Continued)

Acronym or term	Definition
IC	Industry Canada
IF	Intermediate Frequency
IMEI	International Mobile Equipment Identity
IMS	IP Multimedia Subsystem—Architectural framework for delivering IP multimedia services.
inrush current	Peak current drawn when a device is connected or powered on
inter-RAT	Radio Access Technology
IOT	Interoperability Testing
IS	Interim Standard. After receiving industry consensus, the TIA forwards the standard to ANSI for approval.
ISIM	IMS Subscriber Identity Module (Also referred to as a SIM card)
LED	Light Emitting Diode. A semiconductor diode that emits visible or infrared light.
LHCP	Left-Hand Circular Polarized
LNA	Low Noise Amplifier
LPM	Low Power Mode
LPT	Line Print Terminal
LTE	Long Term Evolution—a high-performance air interface for cellular mobile communication systems.
MCS	Modulation and Coding Scheme
MHz	Megahertz = 10e6 Hz
МІМО	Multiple Input Multiple Output—wireless antenna technology that uses multiple antennas at both transmitter and receiver side. This improves performance.
NAS/AS	Network Access Server
NC	No Connect
NIC	Network Interface Card
NLIC	Non-Linear Interference Cancellation
NMEA	National Marine Electronics Association
OEM	Original Equipment Manufacturer—a company that manufactures a product and sells it to a reseller.
OFDMA	Orthogonal Frequency Division Multiple Access
OMA DM	Open Mobile Alliance Device Management—A device management protocol.
OTA	'Over the air' (or radiated through the antenna)

 Table E-1: Acronyms and Definitions (Continued)

Acronym or term	Definition
PA	Power Amplifier
packet	A short, fixed-length block of data, including a header, that is transmitted as a unit in a communications network.
РСВ	Printed Circuit Board
PCC	Primary Component Carrier
PCS	Personal Communication System A cellular communication infrastructure that uses the 1.9 GHz radio spectrum.
PDN	Packet Data Network
PMI	Pre-coding Matrix Index
PSS	Primary synchronisation signal
PST	Product Support Tools
PTCRB	PCS Type Certification Review Board
QAM	Quadrature Amplitude Modulation. This form of modulation uses amplitude, frequency, and phase to transfer data on the carrier wave.
QCI	QoS Class Identifier
QMI	Qualcomm MSM/Modem Interface
QOS	Quality of Service
QPSK	Quadrature Phase-Shift Keying
QPST	Qualcomm Product Support Tools
QZSS	Quasi-Zenith Satellite System—Japanese system for satellite-based augmentation of GPS.
RAT	Radio Access Technology
RF	Radio Frequency
RI	Ring Indicator
roaming	A cellular subscriber is in an area where service is obtained from a cellular service provider that is not the subscriber's provider.
RSE	Radiated Spurious Emissions
RSSI	Received Signal Strength Indication
SCC	Secondary Component Carrier
SDK	Software Development Kit
SED	Smart Error Detection
Sensitivity (Audio)	Measure of lowest power signal that the receiver can measure.

 Table E-1: Acronyms and Definitions (Continued)

Acronym or term	Definition
Sensitivity (RF)	Measure of lowest power signal at the receiver input that can provide a prescribed BER/BLER/SNR value at the receiver output.
SG	An LTE signaling interface for SMS ("SMS over SGs")
SIB	System Information Block
SIM	Subscriber Identity Module. Also referred to as USIM or UICC.
SIMO	Single Input Multiple Output—smart antenna technology that uses a single antenna at the transmitter side and multiple antennas at the receiver side. This improves performance and security.
SISO	Single Input Single Output—antenna technology that uses a single antenna at both the transmitter side and the receiver side.
SKU	Stock Keeping Unit—identifies an inventory item: a unique code, consisting of numbers or letters and numbers, assigned to a product by a retailer for purposes of identification and inventory control.
SMS	Short Message Service. A feature that allows users of a wireless device on a wireless network to receive or transmit short electronic alphanumeric messages (up to 160 characters, depending on the service provider).
S/N	Signal-to-noise (ratio)
SNR	Signal-to-Noise Ratio
SOF	Start of Frame—A USB function.
SSS	Secondary synchronisation signal.
SUPL	Secure User Plane Location
TDD	Time Division Duplexing
TD-SCDMA	Time Division Synchronous Code Division Multiple Access
TIA/EIA	Telecommunications Industry Association / Electronics Industry Association. A standards setting trade organization, whose members provide communications and information technology products, systems, distribution services and professional services in the United States and around the world. Consult www.tiaonline.org.
TIS	Total Isotropic Sensitivity
TRP	Total Radiated Power
UDK	Universal Development Kit (for PCI Express Mini Cards)
UE	User Equipment
UICC	Universal Integrated Circuit Card (Also referred to as a SIM card.)
UL	Uplink (mobile to network)
UMTS	Universal Mobile Telecommunications System
USB	Universal Serial Bus

 Table E-1: Acronyms and Definitions (Continued)

Acronym or term	Definition
USIM	Universal Subscriber Identity Module (UMTS)
VCC	Supply voltage
VSWR	Voltage Standing Wave Ratio
WAN	Wide Area Network
WCDMA	Wideband Code Division Multiple Access (also referred to as UMTS)
WLAN	Wireless Local Area Network
ZIF	Zero Intermediate Frequency
ZUC	ZUC stream cypher

Table E-1: Acronyms and Definitions (Continued)

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