## TOSHIBA BiCMOS Linear Integrated Circuit Silicon Monolithic

## TB6066FNG

## Shock Sensor IC

TB6066FNG detects an existence of external shock through the shock sensor and output Low-level signal at 7 pin.

It has so excellent characteristic in $\mathrm{S} / \mathrm{N}$ ratio that user can use Analog signal for mechanical control systems, like servo control.

## Features

- TB6066FNG operates from 2.7 to 5.5 V DC single power supply voltage.
- Signal from the shock sensor is amplified according to setting gain, and is detected through the internal window comparator.


Weight: 0.07 g (typ.)

- Input terminal of sensor signal is designed high impedance. Differential input impedance $=100 \mathrm{M} \Omega$ (typ.)
- Three Operatinal-Amplifier is built in for design flexibility. (*Note 1)
- Sensitivity of shock detection can be adjusted by external devices.
- Small package: SSOP16-P-225-0.65B ( 0.65 mm pitch)
- Excellent S/N ratio: Improved 10dB compared with our TA6038FN/FNG
*Note 1: LPF (low pass filter) circuitry is not bulit in. User needs to make some filter with one operational-amplifier to cancel the signal of resonant frequency of piezo sensor


## Block Diagram



## Pin Function

| Pin No. | Pin Name |  |
| :---: | :---: | :--- |
| 1 | SIA | Connection terminal of shock sensor |
| 2 | SOA | Amp (A) output terminal |
| 3 | VR | Guard terminal. Reference voltage to protect (1, 16 pin) |
| 4 | A3I | OP-AMP (3) input terminal |
| 5 | A3O | OP-AMP (3) output terminal |
| 6 | CMI | Comparator Input terminal |
| 7 | CMO | Comparator Output terminal (output = "L" when shock is detected.) |
| 8 | GND | Ground terminal |
| 10 | A1O | Power supply voltage |
| 11 | A1I | OP-AMP (1) output terminal |
| 12 | A2O | OP-AMP (2) output terminal |
| 13 | A2I | OP-AMP (2) input terminal |
| 14 | DOB | Differential-Amp output terminal |
| 15 | SIB | Connection terminal of shock sensor |
| 16 |  |  |

## Pin Connection (top view)



Maximum Ratings ( $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Rating | Unit |
| :--- | :---: | :---: | :---: |
| Power supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | 6 | V |
| Input voltage | $\mathrm{V}_{\mathrm{IN}}$ | -0.3 to $\mathrm{V}_{\mathrm{CC}}+0.3$ | V |
| Power dissipation | $\mathrm{P}_{\mathrm{D}}$ | 300 | mW |
| Storage temperature | $\mathrm{T}_{\mathrm{Stg}}$ | -55 to 150 | ${ }^{\circ} \mathrm{C}$ |

## Recommend Operating Condition

| Characteristics | Symbol | Rating | Unit |
| :--- | :---: | :---: | :---: |
| Power supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | 2.7 to 5.5 | V |
| Operating temperature | $\mathrm{T}_{\mathrm{opr}}$ | -25 to 85 | ${ }^{\circ} \mathrm{C}$ |

Note: The IC may be destroyed due to short circuit between adjacent pins, incorrect orientation of device's mounting, connecting positive and negative power supply pins wrong way round, air contamination fault, or fault by improper grounding.

Electrical Characteristics (1) --- Guaranteed data
(unless otherwise specified, $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | - | - | 2.7 | 3.3 | 5.5 | V |
| Supply current | $I_{\text {CC }}$ | 1 | $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}$ | - | 3.5 | 5 | mA |
|  |  |  | $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}$ | - | 3.6 | 5 |  |

(DIFF-AMP)

| Characteristics | Symbol | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Gain | GvBuf | 2 | - | 13.6 | 14 | 14.4 | dB |
| Output DC voltage | VoBuf | 3 | Connect $\mathrm{C}=1000 \mathrm{pF}$ between 1 pin and 2 pin, 15 pin and 16 pin, | 0.7 | 1 | 1.3 | V |
| Output source current | $1 \mathrm{~B}_{\text {so }}$ | 4 | $\mathrm{Voh}=\mathrm{V}_{\mathrm{CC}}-1 \mathrm{~V}$ | 0.6 | 1.9 | - | mA |
| Output sink current | $1 \mathrm{~B}_{\text {si }}$ | 5 | $\mathrm{Vol}=0.3 \mathrm{~V}$ | 70 | 150 | - | $\mu \mathrm{A}$ |

(OP-AMP1)

| Characteristics | Symbol | Test <br> Circuit | Test Condition | Min | Typ. | Max | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input voltage 1 | Vin1 | 6 | - | 1.135 | 1.2 | 1.265 | V |
| Input current | lin | 7 | - | - | 40 | 100 | nA |
| Output voltage range (Low side) | Vol | - | - | 0.3 | - | - | V |
| Output voltage range (High side) | Voh | - | - | - | - | $V_{\mathrm{CC}}-1$ | V |
| Output source current | $\mathrm{IA}_{\text {so }}$ | 8 | Voh $=\mathrm{V}_{\mathrm{CC}}-1 \mathrm{~V}$ | 200 | 800 | - | $\mu \mathrm{A}$ |
| Output sink current | $\mathrm{IA}_{\text {si }}$ | 9 | Vol $=0.3 \mathrm{~V}$ | 100 | 200 | - | $\mu \mathrm{A}$ |

(OP-AMP2)

| Characteristics | Symbol | Test <br> Circuit | Test Condition | Min | Typ. | Max | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input voltage range (Low side) | Vil | - | - | 0 | - | - | V |
| Input voltage range (High side) | Vih | - | - | - | - | $V_{C C}-1$ | V |
| Input current | lin | 10 | Input voltage 1.0 V | -100 | - | 100 | nA |
| Output voltage range (Low side) | Vol | - | - | 0.3 | - | - | V |
| Output voltage range (High side) | Voh | - | - | - | - | $\mathrm{V}_{\mathrm{CC}}-1$ | V |
| Output source current | $\mathrm{IA}_{\text {so }}$ | 11 | Voh $=\mathrm{V}_{\mathrm{CC}}-1 \mathrm{~V}$ | 200 | 800 | - | $\mu \mathrm{A}$ |
| Output sink current | $\mathrm{IA}_{\text {si }}$ | 12 | Vol $=0.3 \mathrm{~V}$ | 100 | 200 | - | $\mu \mathrm{A}$ |

(OP-AMP3)

| Characteristics | Symbol | Test <br> Circuit | Test Condition | Min | Typ. | Max | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input voltage 1 | Vin1 | 13 | - | 1.135 | 1.2 | 1.265 | V |
| Input current | lin | 14 | - | - | 40 | 100 | nA |
| Output voltage range (Low side) | Vol | - | - | 0.3 | - | - | V |
| Output voltage range (High side) | Voh | - | - | - | - | $V_{C C}-1$ | V |
| Output source current | $\mathrm{IA}_{\text {so }}$ | 15 | Voh $=\mathrm{V}_{\mathrm{CC}}-1 \mathrm{~V}$ | 200 | 800 | - | $\mu \mathrm{A}$ |
| Output sink current | $\mathrm{IA}_{\text {si }}$ | 16 | Vol $=0.3 \mathrm{~V}$ | 100 | 200 | - | $\mu \mathrm{A}$ |

(Window-Comparator)

| Characteristics | Symbol | Test <br> Circuit | Test Condition | Min | Typ. | Max | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output pull-up resistance | $\mathrm{RW}_{\mathrm{u}}$ | 17 | - | 21 | 27 | 33 | $\mathrm{k} \Omega$ |
| Output sink current | $\mathrm{IW}_{\mathrm{si}}$ | 18 | Vol $=0.3 \mathrm{~V}$ | 1.0 | 3.0 | - | mA |

(Guard Terminal)

| Characteristics | Symbol | Test <br> Circuit | Test Condition | Min | Typ. | Max |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | Unit | Thef |
| :--- |
| Reference Voltage |

Note: This terminal should be used to make guard ring for (1, 16 pin). Please don't use for any other usage.

## Electrical Characteristics (2) --- Reference data for application (Note)

(DIFF-AMP)

| Characteristics | Symbol | Test <br> Circuit | Test Condition | Min | Typ. | Max |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | Unit | Thn |
| :--- |

(OP-AMP1/2/3)

| Characteristics | Symbol | Test <br> Circuit | Test Condition | Min | Typ. | Max | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Cut-off frequency | fT | - | - | - | - | - | - |
| Openloop gain | Gvo | - | - | -50 | 90 | - | dB |
| Offset voltage (OP-AMP1/3) | Voff | - | - | - | -15 | 0 | 15 |
| Offset voltage (OP-AMP2) | Voff | - | mV |  |  |  |  |

(Window-Comparator)

| Characteristics | Symbol | Test <br> Circuit | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Trip voltage 1 | Vtrp1 | - | - | Vin1 <br> $\pm 0.37$ | Vin1 <br> $\pm 0.4$ | Vin1 <br> $\pm 0.43$ | V |

Note: Toshiba can not test these tables of characteristics for all samples. Therefore Toshiba does not guarantee the data. Please use the data as reference data for customer's application.

## Application Note



Figure 1 The Configuration of G-Force Sensor Amplifier

Figure 1 shows the configuration of G-Force sensor amplifier.
The shock sensor is connected between the pins 1 and 16.
< How to output 0 or 1 from the pin 7 to detect whether there is a shock or not. >

- Using a sensor with the sensitivity Qs (pC/G) to detect the shock g (G). -
a. Setting gain: $\mathrm{C} 1=\mathrm{C} 2(\mathrm{pF}), \mathrm{R} 1(\mathrm{k} \Omega), \mathrm{R} 2(\mathrm{k} \Omega)$

$$
\begin{aligned}
& \frac{\mathrm{Qs} \times \mathrm{g}}{\mathrm{C} 1} \times 2 \times 5 \times \frac{\mathrm{R} 2}{\mathrm{R} 1}=0.4(\mathrm{~V}) \\
& \mathrm{C} 1=\mathrm{C} 2=\frac{\mathrm{Qs} \times \mathrm{g}}{0.04} \times \frac{\mathrm{R} 2}{\mathrm{R} 1}
\end{aligned}
$$

Example: Detecting $5(\mathrm{G})$-shock using a sensor with Qs $=0.34(\mathrm{pC} / \mathrm{G}), \mathrm{R} 1=10(\mathrm{k} \Omega), \mathrm{R} 2=100(\mathrm{k} \Omega)$.
$\mathrm{C} 1=\mathrm{C} 2=\frac{0.34 \times 5}{0.04} \times \frac{100}{10}=425(\mathrm{pF})$
b. Setting the frequency (Hz) of HPF: Setting C3 ( $\mu \mathrm{F}$ ), R1 ( $\mathrm{k} \Omega$ )

$$
\mathrm{fc}(\mathrm{~Hz})=\frac{1}{2 \times \pi \times \mathrm{R} 1 \times \mathrm{C} 3} \times 10^{3}
$$

Example: Setting the frequency to 20 Hz with $R 1=10(k \Omega)$.

$$
\mathrm{C} 3=\frac{1}{2 \times \pi \times 10 \times 20} \times 10^{3}=0.8(\mu \mathrm{~F})
$$

c. Setting the frequency ( kHz ) of LPF: Setting C4 (pF), R2 (k $\Omega$ )

$$
\mathrm{fc}(\mathrm{kHz})=\frac{1}{2 \times \pi \times \mathrm{R} 2 \times \mathrm{C} 4} \times 10^{6}
$$

> | Example: Setting the frequency to 5 kHz with |
| :--- |
| $\mathrm{R} 2=100(\mathrm{k} \Omega)$. |

$$
\mathrm{C} 4=\frac{1}{2 \times \pi \times 100 \times 5} \times 10^{6}=318(\mathrm{pF})
$$

< How to output the voltage according to the shock through the pin 5. >

- Using a sensor with the sensitivity Qs (pC/G), and assuming the shock sensitivity of the system is Vsystem (mV/G). -
a. Setting gain: $\mathrm{C} 1=\mathrm{C} 2(\mathrm{pF}), \mathrm{R} 1(\mathrm{k} \Omega), \mathrm{R} 2(\mathrm{k} \Omega)$

$$
\begin{aligned}
& \frac{\mathrm{Qs}}{\mathrm{C} 1} \times 2 \times 5 \times \frac{\mathrm{R} 2}{\mathrm{R} 1}=\mathrm{V} \text { system } \times 10^{3}(\mathrm{mV} / \mathrm{G}) \\
& \mathrm{C} 1=\mathrm{C} 2=\frac{\mathrm{Qs}}{\mathrm{~V} \text { system }} \times \frac{\mathrm{R} 2}{\mathrm{R} 1} \times 10^{4}(\mathrm{pF})
\end{aligned}
$$

Example: Designing the system with 200 ( $\mathrm{mV} / \mathrm{G}$ ) by using a sensor that $\mathrm{Qs}=0.34(\mathrm{pC} / \mathrm{G})$,
$\mathrm{R} 1=10(\mathrm{k} \Omega), \mathrm{R} 2=100(\mathrm{k} \Omega)$.
$\mathrm{C} 1=\mathrm{C} 2=\frac{0.34}{200} \times \frac{100}{10} \times 10^{4}=170(\mathrm{pF})$

## Equivalent Circuit



## Test Circuit

(1) Supply current: ICC

(2) DIFF-AMP

Gain: GvBuf
Step 1
Gain $=(\mathrm{M} 2-\mathrm{M} 1) /(0.63-0.47)$
Step 2

(3) DIFF-AMP

Output DC voltage: VoBuf

(4) DIFF-AMP

Output source current: IBso

(6) OP-AMP1

Input voltage 1: Vin1

(8) OP-AMP1

Output source current: IAso

(5) DIFF-AMP

Output sink current: IBsi

(7) OP-AMP1

Input current: Iin

(9) OP-AMP1

Output sink current: IAsi

(10) OP-AMP2

Input current: Iin

(11) OP-AMP2

Output source current: IAso

(13) OP-AMP3

Input voltage 1: Vin1

(12) OP-AMP2

Output sink current: IAsi

(14) OP-AMP3

Input current: Iin

(15) OP-AMP3

Output source current: IAso

(17) Window comparator Output pull-up resistance: RWu

(16) OP-AMP3

Output sink current: IAsi

(18) Window comparator Output sink current: Iwsi


## Package Dimensions



Weight: 0.07 g (typ.)

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

