ECN/PCN No.: 3715

For Manufacturer

<table>
<thead>
<tr>
<th>Product Description: SMD Clock Oscillator</th>
<th>Abracon Part Number / Part Series: ASDM</th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
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</table>

<table>
<thead>
<tr>
<th>Affected Revision: L</th>
<th>New Revision: M</th>
<th>Application:</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
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</tr>
</tbody>
</table>

Prior to Change: supply current, rise and fall time, cycle to cycle jitter, disable stand by current

<table>
<thead>
<tr>
<th>Supply Current (no load):</th>
<th></th>
<th>mA</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0 to 39.9999MHz</td>
<td>3</td>
<td>1C</td>
<td></td>
</tr>
<tr>
<td>40.0 to 79.9999MHz</td>
<td>4</td>
<td>1C</td>
<td></td>
</tr>
<tr>
<td>80.0 to 124.9999MHz</td>
<td>5</td>
<td>1C</td>
<td></td>
</tr>
<tr>
<td>125.0 to 150MHz</td>
<td>6</td>
<td>1C</td>
<td></td>
</tr>
</tbody>
</table>

No load
RL=∞
T=25°C

<table>
<thead>
<tr>
<th>Rise Time:</th>
<th>Fall Time:</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Tr</td>
<td>Tf</td>
<td>ns</td>
</tr>
<tr>
<td>1.3</td>
<td>1.3</td>
<td>2.6</td>
</tr>
</tbody>
</table>

15pF; T=25°C
20%/80%×VDD

Cycle to cycle jitter:

95 ps F=100MHz

Disable Stand-by Current:

1 uA

After Change: supply current, rise and fall time, cycle to cycle jitter, disable stand by current

<table>
<thead>
<tr>
<th>Supply Current (no load):</th>
<th></th>
<th>mA</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0 to 39.9999MHz</td>
<td>7</td>
<td>15</td>
<td></td>
</tr>
<tr>
<td>40.0 to 79.9999MHz</td>
<td>8</td>
<td>15</td>
<td></td>
</tr>
<tr>
<td>80.0 to 124.9999MHz</td>
<td>9</td>
<td>15</td>
<td></td>
</tr>
<tr>
<td>125.0 to 150MHz</td>
<td>10</td>
<td>15</td>
<td></td>
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</tbody>
</table>

Vdd=3.3V
No load
RL=∞
T=25°C

<table>
<thead>
<tr>
<th>Rise Time:</th>
<th>Fall Time:</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Tr</td>
<td>Tf</td>
<td>ns</td>
</tr>
<tr>
<td>1.2</td>
<td>1.3</td>
<td>2.0</td>
</tr>
</tbody>
</table>

15pF; T=25°C
20%/80%×VDD

Cycle to cycle jitter:

60 ps F=100MHz

Disable Stand-by Current:

15 uA

Cause/Reason for Change:
Product discontinuation of the first generation internal IC used inside this product series. IC replaced with a new second generation device with lower cycle to cycle period jitter.

Change Plan

Effective Date:
12/17/2020

Additional Remarks:
Revising the existing product series with the latest silicon

Change Declaration:
Existing Abracon part numbers will remain the same. No changes or modifications on the customers BOM is required.

Issued Date:
12/17/2020

For Abracon EOL only

Last Time Buy (if applicable):
Not Applicable

Alternate Part Number / Part Series:
Not Applicable

Additional Approval:
Additional Approval:
Additional Approval:
<table>
<thead>
<tr>
<th>Customer Approval (If Applicable)</th>
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<tbody>
<tr>
<td>Qualification Status:</td>
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<tr>
<td>☐ Approved ☐ Not accepted</td>
</tr>
<tr>
<td>Customer Part Number:</td>
</tr>
<tr>
<td>Customer Project:</td>
</tr>
<tr>
<td>Company Name:</td>
</tr>
<tr>
<td>Company Representative:</td>
</tr>
<tr>
<td>Representative Signature:</td>
</tr>
<tr>
<td>Customer Remarks:</td>
</tr>
</tbody>
</table>
ASDM1-100.000MHZ-LC
ASDM1-100.000MHZ-LC-T
ASDM1-109.2454MHZ-T
ASDM1-121.500MHZ-EC
ASDM1-150.000MHZ-LC
ASDM1-20.000MHZ-LC
ASDM1-20.000MHZ-LC-T
ASDM1-20.000MHZ-T
ASDM1-22.000MHZ-C
ASDM1-22.1184MHZ-LC
ASDM1-23.9818MHZ
ASDM1-24.000MHZ-EC
ASDM1-24.000MHZ-LC
ASDM1-24.000MHZ-LC-T
ASDM1-24.000MHZ-LR-T
ASDM1-24.000MHZ-R-T
ASDM1-24.0649MHZ
ASDM1-24.540MHZ-LC
ASDM1-24.5454MHZ-LC
ASDM1-24.576MHZ-L
ASDM1-24.576MHZ-LC
ASDM1-24.576MHZ-LC-T
ASDM1-25.000MHZ-L
ASDM1-25.000MHZ-LC
ASDM1-25.000MHZ-LC-T
ASDM1-25.000MHZ-LR
ASDM1-25.000MHZ-LR-T
ASDM1-26.000MHZ-LC
ASDM1-26.000MHZ-LC-T
ASDM1-27.000MHZ-L
ASDM1-27.000MHZ-LC
ASDM1-27.000MHZ-LC-T
ASDM1-27.000MHZ-LR
ASDM1-28.6363MHZ-LC
ASDM1-28.636MHZ-LC
ASDM1-29.4912MHZ-LC
ASDM1-29.4912MHZ-LC-T
ASDM1-30.000MHZ-LC-T
ASDM1-32.000MHZ-LC-T
ASDM1-33.000MHZ-LC-T
ASDM1-33.3330MHZ-LC-T
ASDM1-33.333MHZ-LC-T
ASDM1-36.000MHZ-LC
ASDM1-36.000MHZ-LC-T
ASDM1-38.400MHZ-LC
ASDM1-40.000MHZ-LC-T