



# CUSTOMER ADVISORY

## ADV2139

### Intel® Arria® 10 Device and Intel® Cyclone® 10 Device Pin Connection Guidelines Update

#### Description:

Intel® is notifying customers of an update to the following documents:

- Intel® Arria® 10 GT, GX & SX Device Family Pin Connection Guidelines
- Intel® Cyclone® 10 GX Device Family Pin Connection Guidelines

Here is the update listed in the Intel® Arria 10 GT, GX & SX Device Family Pin Connection Guidelines –

- Added Table: 3V Compatible I/O Pins

Here is the update listed in the Intel® Cyclone 10 GX Device Family Pin Connection Guidelines-

- Added Table: 3V Compatible I/O Pins.

The updated Intel® Arria 10 Device Family Pin Connection Guidelines can be found here:

Link:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/dp/aria-10/pcg-01017.pdf>

[https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/hb/stratix-10/s10\\_datasheet.pdf](https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/hb/stratix-10/s10_datasheet.pdf)

The updated Intel® Cyclone 10 GX Device Family Pin Connection Guidelines can be found here:

Link:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/dp/cyclone-10/pcg-01022.pdf>

## Recommended Actions:

Customers are requested to take note of the changes and determine the impact on their designs.

For questions or support, please contact your local Field Applications Engineer (FAE), or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

## Products Affected:

All Intel® Cyclone 10 GX devices and Intel® Arria 10 devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv213/9-opn-list.xlsx>

## Reason for Change:

Addition and clarification of certain device specifications based on latest available data and characterization. There is no change to Arria 10 device and Cyclone 10 device silicon and materials.

## Change Implementation:

**Table 1**

Milestone	Availability
Availability of Intel® Arria 10 Device and Intel® Cyclone 10 GX Device Pin Connection Guidelines Update	Now

## Contact:

For more information, please contact your local Field Applications Engineer (FAE), or submit a question or request at the My Intel support page, log-in via:

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<https://www.intel.com/content/www/us/en/programmable/my-intel/malemailsub/technical-updates.html>

## Revision History

Date	Rev	Description
11/05/2021	1.0.0	Initial Release

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<b>OPN</b>	<b>MM#</b>
10AS016C3U19E2LG	964565
10AS016C3U19E2SG	964674
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10AS016C4U19E3LGAA	99AGK5
10AS016C4U19E3SG	964737
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10AS016C4U19I3SG	964681
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