



Product Change Notification / SYST-02HNSR597

Date:

05-Jun-2023

Product Category:

32-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

Data Sheet - SAM L10/L11 Family Data Sheet

Affected CPNs:

[SYST-02HNSR597_Affected_CPN_06052023.pdf](#)

[SYST-02HNSR597_Affected_CPN_06052023.csv](#)

Notification Text:

SYST-02HNSR597

Microchip has released a new Datasheet for the SAM L10/L11 Family Data Sheet of devices. If you are using one of these devices please read the document located at [SAM L10/L11 Family Data Sheet](#).

Notification Status: Final

Description of Change: Revision H - 06/2023 In addition to the changes listed in the following table, there were numerous typographical updates that were made throughout the document. The following additions or updates were done during this revision:

1) Section Description PAC

- Updated access types in Overview
- Updated the bitfield reset property for the DSU bitfield in the STATUSB Register

2) SUPC

- Updated the text for the BOD33RDY bitfield in the STATUS Register

3) RTC

- Updated the Register properties for the following registers in:
 - CTRLA (COUNT32)
 - CTRLA (COUNT16)
 - CTRLA (Clock/Calendar)
 - COUNT (COUNT32)
 - COUNT (COUNT16)
 - CLOCK (Clock/Calendar)

- 4) DMAC
 - Added new content for Burst Transfer to DMA
- 5) PORT
 - Added a note to Events
- 6) SERCOM USART
 - Added in a new first paragraph for Data Transmission
 - Added a new note to the RXINV and TXINV bitfields for the CTRLA Register
- 7) TC
 - Removed erroneous 8-bit references from Features
 - Clarified verbiage for COUNT8 and COUNT16 in Counter Mode
 - Removed obsolete verbiage from Waveform Output Operations
 - Removed erroneous 8-bit references from Double Buffering
 - Updated the following registers for 8-bit Mode, 16-bit mode and 32-bit mode:
 - CTRLA
 - STATUS
 - WAVE
 - COUNT 8-bit Mode
 - COUNT 16-bit Mode
 - COUNT 32-bit Mode
- 8) TRNG
 - Updated the verbiage in Overview
- 9) ADC
 - Added notes and restructured the content in Conversion Timing and Sampling Rate
- 10) AC
 - Added a new note to Offset Compensation
- 11) Electrical Characteristics at 85
 - Updated voltage number for VPIN in Absolute Maximum Ratings
 - Updated note 3 in Injection Current
 - Updated the Gain conditions in table 46-35 in OPAMP Characteristics
- 12) Schematic Checklist
 - Added new notes to the Introduction
 - Added a new note to External Reset Circuit and removed an obsolete diagram
 - Added a new note to Programming and Debug Ports

Impacts to Data Sheet: See above details

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 05 Jun 2023

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAM L10/L11 Family Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

ATSAML10D14A-MF
ATSAML10D14A-MFT
ATSAML10D14A-MU
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ATSAML10D14A-YF
ATSAML10D14A-YFT
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