

Product Change Notification - SYST-10ZWNL972

Date: 13 Mar 2017
Affected CPNs:  
Notification subject: Data Sheet - KSZ8567R Data Sheet Data Sheet Document Revision
Notification text: SYST-10ZWNL972

Microchip has released a new DeviceDoc for the KSZ8567R Data Sheet of devices. If you are using one of these devices please read the document located at [KSZ8567R Data Sheet](#).

Notification Status: Final

Description of Change:

- 1) Added new section 4.1.10, Remote PHY Loopback and section 4.4.5.1, Two Rate Three Color Marker
- 2) Updated section 4.4.5.2 with additional information on Weighted Random Early Detection (WRED).
- 3) Added additional information to end of section and added new subsections on Standard MIIM and MMD registers on section 4.11.3.1 Standard MIIM Registers (Direct) and section 4.11.3.2, MDIO Manageable Device (MMD) Registers (Indirect)
- 4) Added new section 5.4, MDIO Manageable Device (MMD) Registers (Indirect).
- 5) Added new register definitions on section 5.2.7.8, Port Police Queue Rate Register through section 5.2.7.14, Port WRED Queue Performance Monitor Control Register
- 6) Updated RGMII timing diagrams and data on section 6.4.1RGMII Timing
- 7) Updated/Clarified pin buffer type information on Table 1-2 Buffer Types, Table 3-2 Pin Descriptions and Table 6-1 Electrical Characteristics

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 13 March 2017

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s): [KSZ8567R Data Sheet](#)

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Affected Catalog Part Numbers (CPN)

KSZ8567STXV-TR

KSZ8567STXV

KSZ8567STXI-TR

KSZ8567STXI

KSZ8567RTXV-TR

KSZ8567RTXV

KSZ8567RTXI-TR

KSZ8567RTXI