

Product Change Notification - SYST-27GCUD662

Date:

29 Nov 2018

Product Category:

Ethernet Switches

Affected CPNs:

Notification subject:

Data Sheet - KSZ9897R - 7-Port Gigabit Ethernet Switch with Two RGMII/MII/RMII Interfaces Data Sheet Document Revision

Notification text:

SYST-27GCUD662

Microchip has released a new DeviceDoc for the KSZ9897R - 7-Port Gigabit Ethernet Switch with Two RGMII/MII/RMII Interfaces of devices. If you are using one of these devices please read the document located at [KSZ9897R - 7-Port Gigabit Ethernet Switch with Two RGMII/MII/RMII Interfaces](#).

Notification Status: Final

Description of Change: Below are the changes

Section/Figure/Entry	Correction
Section 8.1, "Package Mark- ing Information," on page 186	Updated top marking information.
Section 8.2, "Package Draw- ings," on page 187	Updated package drawings.
Table 4-16, "Matching Rule Options"	Table updated.
Section 4.4.9, "Tail Tagging Mode," on page 38	Section updated.
Section 4.1.9, "LinkMD® Cable Diagnostics," on page 22	LinkMD details added.
Section 4.4.2.4, "Learning," on page 29	Text correction.
Section 4.4.2.6, "Aging," on page 30	Corrected “time stamp” to “age count” in multiple locations.
Section 5.2.2.5, "PHY Auto- Negotiation Advertisement Register," on page 114	Changed default value of Pause (Flow Control) Capability bit to a note referencing the LED1_1 configuration strap.
Section 5.2.2.10, "PHY 1000BASE-T Control Regis- ter," on page 118	Corrected bit 10 default value. Added information on Test Mode Bits 15:13.
Section 5.2.7.4, "Port Authentication Control Reg- ister," on page 143	Corrected bits 1:0 description.
Section 5.2.2.16, "PHY LinkMD Register," on page 121	Updated register bit descriptions.
Section 5.1.1.4, "Global Chip ID 3 Register," on page 66	Corrected bit 0 description.
Section 5.4, "MDIO Manage- able Device (MMD) Regis- ters (Indirect)," on page 166	Corrected the MMD register read example.
Table 6-2, "RGMII Timing Values"	Revised minimum RGMII TSKEW parameter.
Table 3-3, "Configuration Strap Descriptions"	Corrected swapping of LED2_0 and LED4_0, added notes in strapping. Corrected RXD6_0



	and RXD7_0 in strapping table.
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Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 29 Nov 2018

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

[KSZ9897R - 7-Port Gigabit Ethernet Switch with Two RGMII/MII/RMII Interfaces](#)

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Affected Catalog Part Numbers (CPN)

KSZ9897RTXC

KSZ9897RTXC-TR

KSZ9897RTXI

KSZ9897RTXI-TR