



## Final Product Change Notification

201306008F01

**Issue Date:** 17-Jul-2013

**Effective Date:** 15-Oct-2013

Here's your personalized quality information concerning products from NXP.

For detailed information we invite you to [view this notification online](#)



# QUALITY

### Management Summary

#### Change Category

Wafer Fab process	Assembly Process	Product Marking	Design
Wafer Fab materials	Assembly Materials	Electrical spec./Test coverage	Mechanical Specification
Wafer Fab location	Assembly Location	Test Location	Packing/Shipping/Labeling

## PowerMOS - Additional location for TrenchMOS Diffusion

### Details of this Change

Products made using the T6 TrenchMOS silicon process sourced from Global Foundries, Singapore, have been qualified in to the NXP Manchester UK Fab.

### Why do we Implement this Change

Further to the introduction of dual source supply of T6 LFPACK products from Singapore and Manchester, NXP is now extending this dual source capability to the latest T6 designs in other packages. Dual sourcing provides our customers with enhanced supply chain flexibility and reliability.

### Identification of Affected Products

Top side marking

Devices from Manchester will be identified by `E`  
Devices from Global Foundries, Singapore will be identified by `s`

## Product Availability

### Sample Information

Samples are available from 01-Oct-2013

### Production

Planned first shipment 31-Oct-2013

## Impact

- no impact to the product's functionality anticipated.
- The constituent layers and the physical dimensions of finished devices will be identical.
- The finished products will be electrically identical on all parameters, both static and dynamic.

### Data Sheet Revision

No impact to existing datasheet

### Disposition of Old Products

The Manchester Fab will be introduced as an additional source, therefore products using wafers from both Singapore and Manchester will be shipped in parallel.

## Related Notifications

Notification	Issue Date	Effective Date	Title
201303013F0131	Mar-2013	29-Jun-2013	PowerMOS - Additional location for TrenchMOS diffusion

## Timing and Logistics

Your acknowledgement of this change, conform JEDEC JESD46 D, is expected till 16-Aug-2013.

## Remarks

This product change notification is for your company internal use and for your use as Distributor towards your direct customers. You are responsible for the correct circulation of this information to your direct customers.

NXP does not accept liability for any failure to notify your direct customers of this change within the appropriate timeframe, or for any other incorrect further distribution of this information.

## Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

## About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

A global semiconductor company with operations in more than 25 countries, NXP posted unaudited revenue of \$4.36 billion in 2012.

You have received this email because you are a designated contact or subscribed to NXP's Quality Notifications. NXP shall not be held liable if this Notification is not correctly distributed within your organization.

This message has been automatically distributed. Please do not reply.

[View Notification](#)

[Subscription](#)

[Support](#)

---

[NXP](#) | [Privacy Policy](#) | [Terms of Use](#)

NXP Semiconductors  
High Tech Campus, 5656 AG Eindhoven, The Netherlands  
© 2006-2010 NXP Semiconductors. All rights reserved.