PCN	l Num			230711002				<b>PCN Date:</b> July 12, 202		
Title	e:	Qualification probe (EWS)					e & TI	CDAT-PR as additional waf	er	
Cus	tomer	Contact:	Cha	ange Manag	emen	t team <b>Dept:</b>	Qua	ality Services		
Pro	posed	1 <sup>st</sup> Ship Date	e:	Jan 13, 202	24	Sample requ accepted u		Aug 13, 2023*		
*Sa	mple r	equests receiv	ed	after Aug 13	, 202	3 will not be suppo	orted.			
Cha	nge T	ype:								
$\boxtimes$	_	mbly Site			Des	gn		Wafer Bump Material		
		mbly Process				Sheet		Wafer Bump Process		
$\boxtimes$		mbly Materials				number change		Wafer Fab Site		
		anical Specific		on 🛛		Site		Wafer Fab Materials		
$\boxtimes$		ng/Shipping/L				Process		Wafer Fab Process		
		<u> </u>				N Details				
Des	criptio	on of Change	:							
TI C Sect	DAT-P cion. erial c		l wa		P1 71420	Site for Select Dev	on (141)			
Pro	be Site	e:		Cui	rrent	ı		New:		
	Pr	obe Site (EWS	5)	TI Cla	ark (C	CLARK-PR)	TI	Chengdu (CD-PR)		
Test	cover	age, insertions	s, c	onditions wil	l rem	ain consistent with	curre	ent testing		
Rea	son fo	r Change:								
	,	of supply.	cana	acity to supr	oort h	igh volume ramps.				
						· ·		y (positive / negative):		
Non	-	ou impact on		,, . <u> </u>	ictioi	i, Quality of Relia	a Dillic	(positive / negative):		
Imp	act o	n Environme	nta	Ratings						
								implementation of this ociated environmental rating	gs.	

RoHS	REACH	Green Status	IEC 62474
			☑ No Change

### Changes to product identification resulting from this PCN:

Assembly Site		
Amkor P1	Assembly Site Origin (22L)	ASO: AKR
SCSAT	Assembly Site Origin (22L)	ASO: STS

Sample product shipping label (not actual product label)





(1P) \$N74L\$07N\$R (Q) 2000 (D) 0336 (31T)LOT: 3959047MLA (4W) TKY(1T) 7523483\$12 (P) (2P) REV: (V) 0033317 (20L) C\$0: SHE (21L) CCO:USA (22L) A\$0: MLA (23L) ACO: MYS

**Group 1 Product Affected: Assembly site** 

DS90UB662WRTDRQ1	DS90UB943ARTDTQ1	DS90UB981RTDRQ1	DS90UH943ARTDTQ1
DS90UB662WRTDTQ1	DS90UB960WRTDRQ1	DS90UB981RTDTQ1	DS90UH981RTDRQ1
DS90UB681RTDRQ1	DS90UB960WRTDTQ1	DS90UB983RTDRQ1	DS90UH981RTDTQ1
DS90UB681RTDTQ1	DS90UB962WRTDRQ1	DS90UB983RTDTQ1	DS90UH983RTDRQ1
DS90UB943ARTDRQ1	DS90UB962WRTDTQ1	DS90UH943ARTDRQ1	DS90UH983RTDTQ1

### **Group 2 Product Affected: Assembly site & Probe site**

DP83TC811RWRNDRQ1	DP83TC811RWRNDTQ1	DP83TC811SWRNDRQ1	DP83TC811SWRNDTQ1

## **Qualification Report**

Automotive Product Qualification Summary (As per AEC-Q100, AEC-Q006, and JEDEC Guidelines)

### **Qualification Results**

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: DS90UH983RTDRQ1	QBS Reference: DS90UH941ASRTDTQ1			
Test Gr	Test Group A - Accelerated Environment Stress Tests											
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL3 260C	1 Step	1/0	3/0/0			
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	1 Step	1/22	-			
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	1 Step	1/22	-			
HAST	A2.1	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours	1/77	3/231/0			
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	1/1	3/3/0			
HAST	A2.1.3	-	3	30	Wire Bond Shear, post bHAST, 1X	Post stress	Wires	1/3	3/9/0			

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HAST	A2.1.4		3	30	Bond Pull over Stitch, post bHAST, 1X	Post stress	Wires	1/3	3/9/0
HAST	A2.1.5		3	30	Bond Pull over Ball, post bHAST, 1X	Post stress	Wires	1/3	3/9/0
HAST	A2.2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	192 Hours	1/77	3/231/0
HAST	A2.2.1		3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	1/22	3/66/0
HAST	A2.2.2		3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	1/1	3/3/0
HAST	A2.2.3	•	3	30	Wire Bond Shear, post bHAST, 2X	Post stress	Wires	1/3	3/9/0
HAST	A2.2.4		3	30	Bond Pull over Stitch, post bHAST, 2X	Post stress	Wires	1/3	3/9/0
HAST	A2.2.5	:E)	3	30	Bond Pull over Ball, post bHAST, 2X	Post stress	Wires	1/3	3/9/0
тс	A4.1	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65/150C	1000 Cycles	1/77	3/231/0
тс	A4.1	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65/150C	500 Cycles	1/77	3/231/0
тс	A4.1	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	1000 Cycles	1/77	3/231/0
тс	A4.1	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77	3/231/0
тс	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	1/22	3/66/0
тс	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	1/1	3/3/0
тс	A4.1.3	-	3	30	Wire Bond Shear, post TC, 1X	Post stress	Wires	1/3	3/9/0
тс	A4.1.4	-	3	30	Bond Pull over Stitch, post TC, 1X	Post stress	Wires	1/3	3/9/0
тс	A4.1.5	-	3	30	Bond Pull over Ball, post TC, 1X	Post stress	Wires	1/3	3/9/0
тс	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	1/22	3/66/0
тс	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	1/1	3/3/0
тс	A4.2.3	-	3	30	Wire Bond Shear, post TC, 2X	Post stress	Wires	1/3	3/9/0
тс	A4.2.4	-	3	30	Bond Pull over Stitch, post TC, 2X	Post stress	Wires	1/3	3/9/0
тс	A4.2.5	-	3	30	Bond Pull over Ball, post TC, 2X	Post stress	Wires	1/3	3/9/0
PTC	A5.1	JEDEC JESD22- A105	1	45	PTC	-40/105C	1000 Cycles	1/45	-
PTC	A5.2	JEDEC JESD22- A105	1	45	PTC	-40/105C	2000 Cycles	1/45	-
HTSL	A6.1	JEDEC JESD22- A103	3	45	High Temperature Storage Life	150C	500 Hours	1/45	3/135/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	1/1	3/3/0
HTSL	A6.2	JEDEC JESD22- A103	3	45	High Temperature Storage Life	150C	1000 Hours	1/45	3/135/0
HTSL	A6.2.1		3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	1/1	3/3/0
		Accelerated Lifetime S							
		Package Assembly Inte				Minimum of 5 devices,			
WBS	C1	AEC Q100-001 MIL-STD883 Method	1	30	Wire Bond Shear	30 wires Cpk>1.67  Minimum of 5 devices,	Wires	1/30	3/15/0
WBP	C2	2011	1	30	Wire Bond Pull	30 wires Cpk>1.67	Wires	1/30	3/15/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	1/15	1/15/0

SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	1/15	1/15/0			
PD	C4	JEDEC JESD22- B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	1/10	3/30/0			
Test Gr	Test Group D - Die Fabrication Reliability Tests											
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements			
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements			
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements			
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements			
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements			

QBS: Qual By Similarity

Qual Device DS90UH983RTDRQ1 is qualified at MSL3 260C

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV:125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV:150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

### **Ambient Operating Temperature by Automotive Grade Level:**

Grade 0 (or E): -40C to +150C Grade 1 (or Q): -40C to +125C Grade 2 (or T): -40C to +105C Grade 3 (or I): -40C to +85C

## E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold: HTOL, ED

Room/Hot: THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room: AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

## Qualification Report

## Automotive Product Qualification Summary (As per AEC-Q100, AEC-Q006, and JEDEC Guidelines)

### **Qualification Results**

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: DP83TC811RWRNDRQ1	QBS Reference: DP83TG720SWRHARQ1	QBS Reference: DP83TC811RWRNDRQ1		
Test Gr	Test Group A - Accelerated Environment Stress Tests											
PC	A1	JEDEC J-STD- 020 JESD22- A113	3	77	Preconditioning	MSL3 260C	1 Step	3/0	3/0/0	-		
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	1 Step	3/66	3/66/0	-		
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	1 Step	3/66	3/66/0	-		
HAST	A2.1	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours	3/231	3/231/0	-		

HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	3/3	3/3/0	-
HAST	A2.1.3		3	30	Wire Bond Shear, post bHAST, 1X	Post stress	Wires	3/9	3/9/0	-
HAST	A2.1.4		3	30	Bond Pull over Stitch, post bHAST, 1X	Post stress	Wires	3/9	3/9/0	-
HAST	A2.1.5		3	30	Bond Pull over Ball, post bHAST, 1X	Post stress	Wires	3/9	3/9/0	-
HAST	A2.2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	192 Hours	3/231	3/231/0	-
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	3/66	3/66/0	
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	3/3	3/3/0	-
HAST	A2.2.3	-	3	30	Wire Bond Shear, post bHAST, 2X	Post stress	Wires	3/9	3/9/0	-
HAST	A2.2.4	-	3	30	Bond Pull over Stitch, post bHAST, 2X	Post stress	Wires	3/9	3/9/0	-
HAST	A2.2.5	-	3	30	Bond Pull over Ball, post bHAST, 2X	Post stress	Wires	3/9	3/9/0	-
тс	A4.1	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	3/231	3/231/0	-
тс	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	3/66	3/66/0	-
тс	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	3/3	3/3/0	-
тс	A4.1.3	-	3	30	Wire Bond Shear, post TC, 1X	Post stress	Wires	3/9	3/9/0	-
тс	A4.1.4	-	3	30	Bond Pull over Stitch, post TC, 1X	Post stress	Wires	3/9	3/9/0	-
тс	A4.1.5	-	3	30	Bond Pull over Ball, post TC, 1X	Post stress	Wires	3/9	3/9/0	-
тс	A4.2	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	1000 Cycles	3/231	3/231/0	-
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	3/66	3/66/0	-
тс	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	3/3	3/3/0	-
тс	A4.2.3	-	3	30	Wire Bond Shear, post TC, 2X	Post stress	Wires	3/9	3/9/0	-
тс	A4.2.4	-	3	30	Bond Pull over Stitch, post TC, 2X	Post stress	Wires	3/9	3/9/0	-
тс	A4.2.5	-	3	30	Bond Pull over Ball, post TC, 2X	Post stress	Wires	3/9	3/9/0	-
HTSL	A6.1	JEDEC JESD22- A103	3	45	High Temperature Storage Life	150C	1000 Hours	3/135	3/135/0	-
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	3/3	3/3/0	-
HTSL	A6.2	JEDEC JESD22- A103	3	45	High Temperature Storage Life	150C	2000 Hours	3/135	3/135/0	-
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	3/3	3/3/0	-

		Accelerated								
iest G	roup C - F	Package As	sembly	integrity	lests					
WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90	3/90/0	3/90/0
WBP	C2	MIL- STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90	3/90/0	3/90/0
SD	C3	JEDEC J-STD- 002	1	15	PB Solderability	>95% Lead Coverage	-	1/15	1/15/0	-
SD	C3	JEDEC J-STD- 002	1	15	PB-Free Solderability	>95% Lead Coverage	-	1/15	1/15/0	1/15/0
PD	C4	JEDEC JESD22- B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	3/30	3/30/0	3/30/0
Test G	roup D - [	: Die Fabricat	ion Relia	ability Te	sts	:	:			:
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Proces Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Proces Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Proces Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Proces Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Proces Technology Requirements

QBS: Qual By Similarity

Qual Device DP83TC811RWRNDRQ1 is qualified at MSL3 260C

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of  $0.7 \, \text{eV}$ :  $150 \, \text{C/1k}$  Hours, and  $170 \, \text{C/420}$  Hours

The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C Grade 1 (or Q): -40C to +125C Grade 2 (or T): -40C to +105C Grade 3 (or I): -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold: HTOL, ED

Room/Hot: THB/HAST, TC/PTC, HTSL, ELFR, ESD & LU

Room: AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

## **Qualification Report**

# Automotive Product Qualification Summary (As per AEC-Q100, AEC-Q006, and JEDEC Guidelines)

### **Qualification Results**

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: DP83TC811RWRNDRQ1	QBS Reference: DS90UH941ASRTDTQ1
Test G	roup A - /	Accelerated Environme	nt Stres	s Tests					
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL3 260C	-	1/0/0	3/0/0
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	1/22/0	-
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	1/22/0	-
HAST	A2.1	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours	1/77/0	3/231/0
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	1/1/0	3/3/0
HAST	A2.1.3	-	3	30	Wire Bond Shear, post bHAST, 1X	Post stress	Wires	1/3/0	3/9/0
HAST	A2.1.4	-	3	30	Bond Pull over Stitch, post bHAST, 1X	Post stress	Wires	1/3/0	3/9/0
HAST	A2.1.5	-	3	30	Bond Pull over Ball, post bHAST, 1X	Post stress	Wires	1/3/0	3/9/0
HAST	A2.2	JEDEC JESD22- A110	3	70	Biased HAST	130C/85%RH	192 Hours	1/77/0	3/231/0
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	1/22/0	3/66/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	1/1/0	3/3/0
HAST	A2.2.3	-	3	30	Wire Bond Shear, post bHAST, 2X	Post stress	Wires	1/3/0	3/9/0
HAST	A2.2.4	-	3	30	Bond Pull over Stitch, post bHAST, 2X	Post stress	Wires	1/3/0	3/9/0
HAST	A2.2.5	-	3	30	Bond Pull over Ball, post bHAST, 2X	Post stress	Wires	1/3/0	3/9/0
тс	A4.1	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65/150C	500 Cycles	-	3/231/0
тс	A4.1	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	-
тс	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	1/22/0	3/66/0
тс	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	1/1/0	3/3/0
тс	A4.1.3	-	3	30	Wire Bond Shear, post TC, 1X	Post stress	Wires	1/3/0	3/9/0
тс	A4.1.4	-	3	30	Bond Pull over Stitch, post TC, 1X	Post stress	Wires	1/3/0	3/9/0
тс	A4.1.5	-	3	30	Bond Pull over Ball, post TC, 1X	Post stress	Wires	1/3/0	3/9/0

тс	A4.2	JEDEC JESD22- A104 and Appendix 3	3	70	Temperature Cycle	-65/150C	1000 Cycles	-	3/231/0
тс	A4.2	JEDEC JESD22- A104 and Appendix 3	3	70	Temperature Cycle	-65C/150C	1000 Cycles	1/77/0	-
тс	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	1/22/0	3/66/0
тс	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	1/1/0	3/3/0
тс	A4.2.3	-	3	30	Wire Bond Shear, post TC, 2X	Post stress	Wires	1/3/0	3/9/0
тс	A4.2.4	-	3	30	Bond Pull over Stitch, post TC, 2X	Post stress	Wires	1/3/0	3/9/0
тс	A4.2.5	-	3	30	Bond Pull over Ball, post TC, 2X	Post stress	Wires	1/3/0	3/9/0
HTSL	A6.1	JEDEC JESD22- A103	3	45	High Temperature Storage Life	150C	1000 Hours	1/45/0	-
HTSL	A6.1	JEDEC JESD22- A103	3	45	High Temperature Storage Life	150C	500 Hours	-	3/135/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	1/1/0	3/3/0
HTSL	A6.2	JEDEC JESD22- A103	3	44	High Temperature Storage Life	150C	1000 Hours	-	3/135/0
HTSL	A6.2	JEDEC JESD22- A103	3	44	High Temperature Storage Life	150C	2000 Hours	1/45/0	-
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	1/1/0	3/3/0
Test Group B - Accelerated Lifetime Simulation Tests									
Test Group C - Package Assembly Integrity Tests									
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/15/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/15/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	1/15/0	1/15/0
SD	СЗ	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	1/15/0	1/15/0
PD	C4	JEDEC JESD22- B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	3/30/0	3/30/0
Test Group D - Die Fabrication Reliability Tests									
ЕМ	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
								Completed Per Process	Completed Per Process
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Technology Requirements	Technology Requirements
NBTI	D3	JESD60 & 28	-	-	Hot Carrier Injection  Negative Bias Temperature Instability	-	-		
		JESD60 & 28	-	-	Negative Bias Temperature	-		Requirements  Completed Per Process Technology	Requirements  Completed Per Process Technology

QBS: Qual By Similarity

Qual Device DP83TC811RWRNDRQ1 is qualified at MSL3 260C

 $\label{lem:preconditioning} \ \text{was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle,} \ \ \text{Thermal Shock, and HTSL, as applicable}$ 

The following are equivalent HTOL options based on an activation energy of 0.7eV:125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV:150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

### Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C Grade 1 (or Q): -40C to +125C Grade 2 (or T): -40C to +105C Grade 3 (or I): -40C to +85C

## E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold: HTOL, ED

Room/Hot: THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room: AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

ZVEI ID reference: SEM-PA-18, SEM-PA-07, SEM-PA-11, SEM-TF-01

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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